

This training course provides service technician training for the Sirius-PJ1 series.

This course will cover information related to service. To understand the features of the machine, the correct ways to turn the projector on or off, about power saving modes, and other matters that are related to operation, please study the user guide.

Version 1.01: The file sizes of some inserted photos were reduced.



This section provides an overview of the machine, and the options that can be installed.



	Sirius-PJ1 nx	Sirius-PJ1 nw			
Type of Projector	LCD				
Brightness	6000 lm	5500 lm			
Lamp type	330W mer	cury lamp			
Resolution	XGA	WXGA			
Keystone	Vertical and horizontal, manual				
Projection Screen Size	30" - 500" (76.2 – 1270 cm)				
Projection Distance (with standard lens)	Sirius-PJ1 nx: 1.2 to 30.8 m (3.93 to 101 ft) Sirius-PJ1 nw: 1.3 to 32.4 m (4.26 to 106.3 ft)				
Dimensions (W × D × H)	499 × 359 × 144				
Weight	8.54 kg (18.8 lbs)				
Power Consumption	464W				
Speaker	10W x 1				
Wireless LAN	Option (USB)				
Wired LAN	Ye	es			
USB	Ye	es			
HDMI	Ye	es			

□ See the specifications table in the service manual for more details.

#### **Features**

- □ This product can be installed by users, except when mounted on a ceiling.
- □ This product is designed for user maintenance. Regular on-site maintenance is not needed.
- □ LEDs show the symptoms for troubleshooting (blinking/lit, number of times the LEDs blink, etc).
- □ A service mode is available.

No additional notes





□ Note that in the service mode menu, Eco Mode is referred to as 'Low Mode'.



- □ Wireless LAN Unit M1: For USA, Canada, Mexico, Brazil. Colombia, and Taiwan
- Wireless LAN Unit M2: For CE Countries (27 EU countries, and Iceland, Liechtenstein, Norway, and Switzerland), UAE, Saudi Arabia, Oman, South Africa, Turkey, Egypt, Israel, Australia, NZ, Thailand, HK, Singapore, Malaysia, Sri Lanka, Pakistan, Vietnam, India, Philippines, Peru, Chile, Argentina, Ecuador
- □ Wireless LAN Unit M3: For Russia













- 1. Remote Sensor (rear)
- 2. Lens Shift Dial (horizontal): This moves the projected image left/right
- 3. Ventilation (outlet)
- 4. Lens Shift Dial (vertical): This moves the projected image up/down
- 5. Lamp Cover
- 6. Control Panel
- 7. Security Bar: Users can attach an anti-theft device here
- 8. Adjustable Tilt Foot
- 9. Indicator Section
- 10. Remote Sensor (front)
- 11. Zoom Lever/Zoom Ring
- 12. Focus Ring
- 13. Lens
- 14. Lens Release Button
- 15. Mono Speaker
- 16. Lens Cap



1. Ventilation (inlet) / Filter Cover: The wireless LAN unit's USB (LAN) port is located inside here.

- 2. Ventilation (outlet)
- 3. Terminal Panel
- 4. AC Input
- 5. Built-in Security Slot



- □ For details: Service manual > 1. Product Information > Overview
- □ 5. The left and right arrow buttons can be used to adjust the speaker volume.



- This slide shows the Source Menu, and explains the symbols that appear on the menu screen.
- □ For details of all functions, see the user's manual.
- ❑ High Altitude Mode: The fans operate at a higher speed in this mode. Use this when the projector's location is more than 1500 m (5500 ft) above sea level (otherwise, the projector could overheat and shut down automatically). If you use Eco Mode, also use High Altitude Mode if the projector's location is more than 1000 m (3200 ft) above sea level.
  - Also, if you use high altitude mode at less than 1500 m (5500 ft) above sea level, the projector could become too cool, causing images to flicker. Switch [FAN MODE] to [AUTO].
- High Temperature Alert: High temperature detected inside the projector, Eco Mode is enforced
  - If the projector overheats, it shuts down automatically. Wait a few moments then turn back on again.

		Me	nus		
SOURCE ADJUST	SETUP INFO. ONS • VIDEO	RESET	SOURCE ADJUST < BASIC • MENU • INSTALLA	SETUP INFO. ATION(1) ►	RESET
MODE PRESET DETAIL SETTINGS CONTRAST BRIGHTNESS SHARPNESS COLOR HUE RESET	STANDARD 2:PRESENTATION	<ul> <li>(1)</li> <li>(5)</li> <li>(5)</li></ul>	KEYSTONE CORNERSTONE PIP/PICTURE BY PICTUR WALL COLOR ECO MODE CLOSED CAPTION OFF TIMER TOOLS LANGUAGE	e OFF OFF OFF OFF ENGLISH	
SOURCE ADJUST	SETUP	MOVE ADVANCED RESET	ENTER ::SELECT ENT::E	SETUP INFO,	ADVANCED
USAGE TIME      SOURCE(     LAMP LIFE REMAINING     LAMP HOURS USED     FILTER HOURS USED     TOTAL CARBON SAVING	1) • SOURCE(2) • 00000 [H] 00000 [H] 35 0.000[kg-CO2]	(1/3) ⇒ (100%)	CURRENT SIGNAL ALL DATA ALL DATA (INCLUDING E CLEAR LAMP HOURS CLEAR FILTER HOURS	ENTRY LIST)	
	€EXIT \$:MOVE «	MOVE ADVANCED	(ENTER):SELECT (ENT):E	EXIT \$:MOVE	•:MOVE ADVANCED

- □ This slide shows the other four menu tabs.
- □ Note that in the Adjust, Setup, and Info tabs, there is more than one page.
  - For example, in the Adjust tab, there are three pages: Picture, Image Options, and Video.



- 1. LAN Port (RJ-45)
- 2. HDMI IN Connector (Type A)
- 3. DisplayPort IN Connector
- 4. USB Port (Type A)
- 5. MONITOR OUT (COMP. 1) Connector (Mini D-Sub 15 Pin)
- 6. COMPUTER 1 IN/ Component Input Connector (Mini D-Sub 15 Pin)
- 7. COMPUTER 1 AUDIO IN Mini Jack (Stereo Mini)
- 8. PC CONTROL Port (D-Sub 9 Pin): Use this port to connect a PC or control system. This enables the user to control the projector using serial communication protocol. Be sure to use a cross cable.
- 9. COMPUTER 2 IN / Component Input Connector (Mini D-Sub 15 Pin)
- 10. COMPUTER 2 AUDIO IN Mini Jack (Stereo Mini)
- 11. Service Connector (Stereo Mini)
- 12. COMPUTER 3 AUDIO IN Mini Jack (Stereo Mini)
- 13. AUDIO OUT Mini Jack (Stereo Mini)
- 14. COMPUTER 3 IN/Component (R/Cr, G/Y, B/Cb, H, V) Connectors (BNC × 5)
- 15. VIDEO/S-VIDEO AUDIO IN L/MONO, R (RCA)
- 16. VIDEO IN Connector (RCA)
- 17. S-VIDEO IN Connector (Mini DIN 4 Pin)



Please ignore the callouts [A] and red circles in the diagram.



Please ignore the callouts [A] and red circles in the diagram.

#### **Iris Unit**

□ The Iris unit physically blocks the light path from the projector to the screen when the projector is on but nothing is being projected.

At these times during a presentation, a blank white screen is often not desirable. So, there are three ways to get around this.

- 1. Turn off the projector. If you do that, you have to think about cool-down and warm-up times. Also, if you keep turning the lamp off/on, the lamp's life can be reduced.
- 2. Send an all-black video signal. This can cause delicate optical components to overheat, reducing the projector's life
- 3. Physically block the light path inside the projector. The projector keeps working as normal but nothing comes out of the projector lens. This is how the Iris unit works.

□ This is the first Ricoh projector to contain an Iris unit.

#### No additional notes

Slide 22



This section explains the basic points about servicing the machine.





□ Service manual: 3. Replacement and Adjustment > Special Tools





The next few slides explain what software must be installed, and how to install it. Using the software will be explained later, in the 'Adjustments after Replacing Components' section.





□ In the data file names, '\*\*\*' denotes the version.





□ The name of the service adjustment software (TBA.exe) was not decided when this TTP was made.





□ Service manual > Troubleshooting > Service Mode > Mode change

PAGE1 + PAG STATUS 1. E1-1 2. E4-1 3. 4. 5.	GE2 + RESET PJ USABE 00002[H] 00002[H]	TEMP 100/100 100/100		PAGE1 STA 1. E1 2. E4 3. 4. 5.	• PABE2 TUS   -1	2 • RESET PJ USAGE 00002[H] 00002[H]	TEMP 100/100 100/100	OTHER NACOXOOOO
	COD:EXIT		**:MOVE	_	ş			**:MOVE
	Exp	ert Mode			1	Servi	ce Mode	MINOYE
ר The	Error		on on Pag	. 1 .	.f E.	mort N	lada ar	Dago 1
⊐ The of S	e Error I Service	Log is se Mode.	een on Pag	e 1 c	of Ex	kpert N	lode or	Page 1
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□ Service manual: Troubleshooting > Service Mode








This section explains the most important points about replacing components.







Please ignore the callouts [A] and red circles in the diagram.



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- Adjustment Procedures: Service manual > Electrical Adjustment
- □ We will look at these adjustments and how to use the software later.
- Service adjustment software: TBA.exe the name of this software is not decided yet.



- Adjustment Procedure: Service manual > 3. Replacement and Adjustment > Replacement of Optical Parts > Optical Parts Adjustment > Adjustment of the optical axis (Shadow adjustment)
- □ We will look at these adjustments later.



- Replacement Procedure: Service manual > 3. Replacement and Adjustment > Replacement of Optical Parts
- Adjustment Procedure: Service manual > 3. Replacement and Adjustment > Replacement of Optical Parts > Optical Parts Adjustment > Adjustment of the polarization plate (Contrast adjustment)
- □ We will look at these adjustments later.





This section explains the most important points about adjustments that are needed after replacing components.



- □ We discussed the software needed for inputting data earlier in the course. In this section, we shall see how to use it.
- Optical axis adjustment: After replacing the OPT BASE, check and only do the adjustments if required.



The component marked with a red cross in the diagram on the left (Iris unit) actually has a longer cable than shown, and does not need to be pulled out of the machine.





- □ FL1, RL2, and M1 refer to various components in the machine, which you will move during the adjustment.
  - > FL1: Field lens 1
  - > RL2: Relay lens 2
  - > M1: Mirror 1





Adjustment Procedure: Service manual > 3. Replacement and Adjustment > Replacement of Optical Parts > Optical Parts Adjustment > Adjustment of the optical axis (Shadow adjustment)



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Adjustment Procedure: Service manual > 3. Replacement and Adjustment > Replacement of Optical Parts > Optical Parts Adjustment > Adjustment of the optical axis (Shadow adjustment)











□ We will look at this procedure in more detail later.



- □ Adjustment Procedures: Service manual > Electrical Adjustment
- □ We will look at these procedures in more detail later.



□ The name of the service adjustment software (TBA.exe) was not decided when this TTP was made.





This software does not write the data you just copied from the old board. This software copies firmware and other model-specific data to the new board.









Extended display identification data (EDID) is data which describes the capabilities of a video output device, such as a projector, to a source of video data, such as a computer.













No additional notes



□ The name of the service adjustment software (TBA.exe) was not decided when this TTP was made.


- In normal standby mode, the Power indicator is orange and the Status indicator is green.
- □ In power-saving standby mode, the Power indicator is red and the Status indicator is off.
- □ If the projector is not in standby mode, press the Power button one time.



□ VT Data, Uniformity Data, Color Correction Data, Multi Data were input automatically when you used PJupgrader2 (see Data Writing After Replacing the Main Board – 4).



□ The name of the service adjustment software (TBA.exe) was not decided when this TTP was made.





□ The values displayed by the side of each scroll bar show the original value to the left of the arrow, and the current value to the right of the arrow.



No additional notes



- □ In normal standby mode, the Power indicator is orange and the Status indicator is green.
- □ In power-saving standby mode, the Power indicator is red and the Status indicator is off.
- □ If the projector is not in standby mode, press the Power button one time.



This section explains the basic points about updating the firmware.

□ Service Manual Procedure: 4. System Maintenance > Firmware Update

		Updating the System Firmware
		Projector Update
		Projector Update Firmware/Data: UPDATE REBOOT Status: To Download Page ( Global Site / Japanese Site )
		Check the IP address of the projector.
	-	In the following example, the IP address is xxx.xxx.xxx
		Open a browser, and input the following:
		Click [Refer].
		Select the file to be overwritten, then click [Update], then [OK]. <ul> <li>'File written successfully' will be displayed after the update is finished.</li> </ul>
		Check the history information that appears on the screen.
		If you have more firmware files to update, select another file and click [Update].
		<ul> <li>Click [Reboot] when you have finished.</li> <li>Do not turn the power off or disconnect the LAN cable before rebooting.</li> </ul>
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□ The detailed procedure is in the System Maintenance - Firmware Update section of the service manual.



This section contains technical information on how the electronics in this model work.

□ In the following slides, the main part of the slide shows a circuit diagram, and the notes page contains the description.

#### Synchronization Signal Processing



- The sync signal (H/V or CS) which is input from COMPUTER 1 (D-SUB15pin M1702), and COMPUTER 2 (D-SUB15pin M1701) is input to A/D Converter (IC2003), respectively. The COMPUTER 3 (BNC x5 M1001) signal is also input to the A/D Converter via POIO2 connector from the IO\_PWB. The SOG (Sync on Green) signal is branched and also input to the SOG-exclusive input port of the A/D Converter.
- □ With this A/D Converter, the following synchronization signals are processed.
  - Horizontal/vertical synchronizing separation
  - > Sync separation of composite synchronization, Sync on Green.
  - > Existence of horizontal/vertical sync signal and polarity discrimination
  - Clamp pulse generation and processing
- □ The information obtained here is read into SyncDet (IC2801) via the I2C-BUS, and is stored into a register which is readable by the CPU. Also, the sync signal of the terminal selected with the projector MENU is sync-separated and becomes the horizontal/vertical sync signal and FIELD signal.



- The sync signal (H/V or CS) which is input from COMPUTER 1 (D-SUB15pin M1702) and COMPUTER 2 (D-SUB15pin M1701) is input to the A/D Converter (IC2003), respectively. The COMPUTER 3 (BNC x5 M1001) signal is also input to the A/D Converter via the POIO2 connector from the IO\_PWB.
- The video signal is converted to a 10-bit digital signal with the internal A/D Converter. This A/D Converter is also has a built-in PLL circuit, and the clock locked with H-sync signal and timing pulse are internally generated. It outputs the video signal from the terminal selected with the projector MENU to SyncDet (IC2801) after A/D conversion.



- The video signal which is input to COMPUTER 1 (M1702) is transformed to 6dB AMP in EL5306 (IC1706), then outputs to the MONITOR OUT terminal (M1003) of the IO\_PWB board via POIO1 connector. The sync signal goes through the buffer circuits, Q1703, Q1704, Q1705, Q1706, and outputs to the MONITOR OUT terminal of the IO\_PWB board via the POIO1 connector.
- Monitor output is possible when Standby Mode of the projector is set to Normal. However, when Power-Saving or Network Standby is selected, the monitor output is not enabled when the status is changed to their Standby Mode.

#### HDMI Digital Signal Processing



- The TMDS signal which is input to the HDMI terminal is transmitted to the HDMI Receiver IC (IC2203) and converted to RGB 10-bit digital video signals, clock signal, horizontal/vertical sync signal, DE signal, and FIELD signal, then input to SyncDet (IC2803).
- In addition, the audio signal is converted to Master Clock (MCLK), Bit Clock (BCLK), Word Clock (WCLK), and Data (DO), then input to SyncDet.
- This Receiver IC is equipped with a HDCP function. If the video output device is also equipped with a HDCP function, the encryption unlock is processed between the video output device and the Receiver IC, and when encryption is unlocked, the device can output the video. If not unlocked, the video will not be output.
- Also, the Receiver IC has a built-in memory area for EDID.



- □ The digital signal which is input to the Display Port terminal is transmitted to the Display Port Receiver IC (IC2404) and converted to RGB 10-bit digital video signals, clock signal, horizontal/vertical sync signal, DE signal, and FIELD signal, then input to SyncDet (IC2803).
- In addition, the audio signal is converted to Master Clock (MCLK), Bit Clock (BCLK), Word Clock (WCLK), and Data (DO), then input to SyncDet.
- This Receiver IC is equipped with a HDCP function. If the video output device is also equipped with a HDCP function, the encryption unlock is processed between the video output device and the Receiver IC, and when encryption is unlocked, the device can output the video. If not unlocked, the video will not be output.
- In addition, the external EEPROM (IC2403) stores EDID data and firmware for the Display Port.



- The VIDEO signal, S-VIDEO signal, and the signal from COMPUTER3 (M1001) in VIDEO Mode is input to the 3ch Video Switch (IC2502) provided on the MAIN\_PWB via the POIO2 connector. The CPU controlling CC\_SEL1 and CC\_SEL2 selects the terminal, and input to Video Decoder (IC2509). However, the S-VIDEO color signal is directly input to the Video Decoder.
- In the Video Decoder, color system recognition, horizontal lock detection, vertical frequency detection are performed and transmitted to SyncDet (IC2801) via the I2C-BUS, then stored into a register which is readable by the CPU.
- The video signals, Y/C separation of NTSC, 3LINE Y/C separation of PAL using external SDRAM (IC2510), and Y/C separation of SECAM using BPF&TRAP are processed.
- □ The video output from the video decoder is ITU-R BT.656 4:2:2 10bit, and input to SyncDet.
- Also, this projector supports closed caption. The video signal is input to the Micro Control Unit which is customized for the closed caption decoding via 6dB AMP (IC2507). The decoded signal is synchronized with the sync signal from the SyncDet and output. Then it is layered on the video outputs.



- The serial control terminals, COMPUTER1 (M1702) and COMPUTER2 (M1701), are connected to the EEPROMs for Plug and Play, COMPUTER1 for IC1701, COMPUTER2 for IC1702, respectively. This enables the reading PC to read EDID information and detect the projector. (COMPUTER3 is not supported.)
- □ The HDMI terminal (15,16 pin) is connected to the HDMI Receiver IC (IC2203). This enables the PC to read EDID information and detect the projector.
- For the Display Port, when the Display Port Receiver IC (2404) is powered, it reads EDID data from the external EEPROM (IC2403) and stores it in the Display Port Receiver IC. Using the AUX Channel of the Display Port terminal (pins 15, 17), EDID information stored in the Display Port Receiver IC is read. This enables the PC to detect the projector.
- □ This projector's Plug and Play corresponds to DDC/2B.
- In addition, using a dedicated tool for the EDID writing, writing in COMPUTER1: EEPROM (IC1701), COMPUTER2: EEPROM (IC1702), HDMI: HDMI Receiver IC (IC2203), Display Port: EEPROM (IC2403) are possible via the route of PC, CPU, SyncDet (IC2801).
- □ It is only possible via PC. Direct writing from each terminal is not possible.



- The audio signals which are input from COMPUTER 1 (M5401), COMPUTER 2 (M5402), COMPUTER 3 (M1002), and VIDEO (M1005) are input to the Audio Processor IC (IC5401).
- The audio signals of HDMI and Display Port are output from each Receiver IC (IC2203/2404) in I2S (MCLK/BCLK/WCLK/DO), and once input to SyncDet (IC2801). Then only the I2S of the terminal selected by the projector menu is output. The output I2S is Wired OR with I2S output from the MM board and input to DAC (IC5202). The D/A conversion is processed with this audio DAC and input to the audio processor IC.
- In the audio processor IC, only the audio signal from the selected terminal by the projector menu is output. Then L/R signal multiple processing is performed and input to the audio amplifier (IC1201). Then after amplification, the signal is output from the monaural speaker (8Ω) (MAX: 10W).
- □ When a cable is connected to the Audio Out terminal, the speaker will be muted, and only the L/R signal which is output from the audio processor IC will be output to the Audio Out terminal.
- The audio output is possible when Standby Mode of the projector is set to Normal. However, when Power-Saving or Network Standby is selected, the output is not enabled when the status is changed to their Standby Mode.
- The VOLUME control is performed by controlling the audio processor IC from CPU (IC4001) via I2C.
- This projector is equipped with a beep function on start-up or key operation. The beeping is achieved by the multiple processing of PWM output of the TightCell2 (IC4502) connected to the Local BUS of the CPU with the audio amplifier input.



- SyncDet(IC2801) is FPGA. It will function on start-up by being configured by the CPU (IC4001).
- The digital video output from the A/D Converter (IC2003), HDMI Receiver IC (IC2203), Video Decoder (IC2509), Display Port Receiver IC (IC2404), and MM CPU (IC9201) are input to SyncDet in the ratio of 1:1, and the signal from the terminal selected by the menu of the projector is output to the REON A Port. When the functions of PIP (Picture in Picture) and PBP (Picture by Picture) are used, the Video Decoder output is also output to the REON B Port.
- When the Closed Caption function is enabled, the captions which are output from MCU (IC2506) and the video output from the Video Decoder are layered in SyncDet.
- This IC calculates required information from the sync signals and also by reading from each device via I2C. The CPU reads the information via the Local Bus to recognizs the signals. After that, each device is controlled by I2C via SyncDet, and performs adjustment following the result of the signal recognition. However, adjustment will not be performed for the output from the Multi Media CPU, since its output signal is fixed.
- □ Also, the CPU performs adjustment by I2C control via SyncDet, when the video adjustment function is used by the user.
- □ This IC has 2 circuits of 3-wire interfaces, and the CPU is enabled to control the downstream Combine2 and LCD Driver via SyncDet.



- REON (IC3001) is a LSI which has a integrated function of CPU, video signal processing circuit, OSD generation/multiple processing, USB, and peripheral functions (I2C, UART, GPIO). It is mainly used for various video signal processing, including resolution conversion, keystone correction, sharpness, OSD, and PIP/PBP.
- □ With the peripheral devices, a 128Mb Flash ROM (IC3102) in which firmware is stored, and 512Mb DDR2 SDRAM x4 (IC3103, IC3104, IC3105, IC3106) as work memory for the video signal processing are connected. For the communication with the CPU, 2nd circuit serial interfaces are used.
- □ The video signal which is output from SyncDet (IC2801) is processed in this order: sharpness, resolution conversion, OSD generation/multiple processing, keystone correction. Then a 10-bit 3ch video signal is output to Combine2.



- □ Combine2 (IC6005) is an FPGA. It functions by reading data stored in the SPI Flash ROM (IC6004) and performs configuration.
- □ The Combine2 performs the following color adjustments.
  - Color correction
  - Color temperature setting
  - Gamma correction
  - Wall color correction
- □ The APL/histogram detection for IRIS control is also performed with this IC.
- □ The 10-bit 3ch video signal and sync signal output from REON (IC3001) are resynchronized with SSCLK (XGA: 65MHz, WXGA: 85MHz), spread spectrum signal, output from Clock Generator (IC4702). Then the signal is output as a 10bit 3ch video signal.



- The video signal and sync signal output in 10-bit 3ch from Combine2 (IC6005) is input to the digital signal driver/timing generator IC (IC7001) for the liquid crystal panel. Then it is output to the liquid crystal panel driver IC (IC7201, 7301, 7401) passing through each correction circuit, including V-T correction, color shading correction.
- In the V-T correction part, the input signal is converted to the data subject to characteristics of the liquid crystal panel according to the lookup table. In the color shading correction part, arithmetic processing is performed on the video signals, so that correction data provided at each point in the window which is divided into 29 (horizontally) times 17 (vertically) in case of XGA, and 30 (horizontally) times 17 (vertically) in case of WXGA, is equalized.
- □ The digital signal driver/timing generator IC for the liquid crystal panel is controlled by the 3-wire serial interface from SyncDet (IC2801).
- □ The 12-bit 3ch 2-phase video signals output from the digital signal driver/timing generator IC for the liquid crystal panel are converted to 12-phase analog signals, in packs of RGB colors after D/A, and level conversion by the liquid crystal panel driver IC, and supplied to the liquid crystal panel.
- The timing signal driving for the liquid crystal panel is configured by the digital signal driver/timing generator IC for the liquid crystal panel, and the output timing signal will be supplied to the liquid crystal panel. The common voltage (VCOM) for the liquid crystal panel is supplied to each panel using a dedicated terminal for the liquid crystal panel driver IC. Each common voltage is set by I2C control from the digital signal driver/timing generator IC for the liquid crystal panel.



- When Viewer and Network are selected in the menu of the projector, the video output from the MM board is output to the MAIN board via POMM2 (70 pin connector) to project the video output.
- □ The interface with the MAIN board is performed via POMM2 (70 pin connector). The main signals to the POMM2 connector are the power supply, the control signal, the video signal, and the audio signal. The communication (control signal) with the MAIN board are possible with 2 circuits, USB and UART.
- □ The main circuit configuration of the MM board is as follows;
  - Power circuit: Generates electric power required in the MM board from +6,2V.
  - > CPU circuit: Consists of CPU, NAND Flash, and DDR2SD RAM.
  - > USB circuit: Consists of CPU, USB-HUB, Hi-Side SW, and USB terminal.
  - > LAN circuit: Consists of CPU, LAN-PHY, and RJ-45 terminal.
  - > RTC circuit: Consists of RTC-IC (Real Time Clock).

#### RICOH Multi Media (MM) Processing Unit **Power Circuit** 6.2V 5V IC9001 For USB NJM2819ADK3-05 5V/2A (Fixed Voltage) FN 1.05\ 109005 3.3V BD00HC0WEF For I/F (USB/LAN) 1.225V 1.8 3.3V/2A 3.3V 2.6V 1.8\/ IC9008 MC13892VL 1.8\ 1.25V 2.775V For CPU 1C9002 5\ IC9013 3.6V NJM2819ADK3-05 BD00C0AWEF (Mair NAND, Powe 5V/2A 3.6V/1A Power) etc. FN EN (Fixed Voltage) Management IC 5V For RTC

- □ The MM board operates by +6.2V supplied from POMM2 (70-pin connectors) 1 to 5, 36 to 40 pin. And for RTC, operates on +5V supplied from pins 42, 45, 48.
  - IC9001: 5V regulator IC (Power supply for USB VBUS(5V))
  - IC9005: 3.3V regulator IC (Power supply for USB-PHY, USB-HUB, LAN-PHY)
  - > IC9002: 5V regulator IC (Upstream of IC9013)
  - IC9013: 3.6V regulator IC (Supply for IC9008)
  - > IC9008: PMIC (Generates required power of the CPU.)
- □ The ON/OFF control of IC9001, IC9002, and IC9013 is performed by the MM\_PWON signal from the MAIN board.
- □ When the MM\_PWON signal is "L", the power of the MM board is OFF. However, 5V for RTC is always ON.
- IC9005 and IC9013 are voltage-controlling type regulator ICs, and the output voltage is set by the external resistance. (IC9005: Set at 3.3V output, IC9013: Set at 3.6V output)
- □ The Power IC IC9008 is an exclusive power IC for the CPU, 3.6V single power supply. The power required for the CPU is output following the activation sequence in the order of power supply start. Also on start-up, it outputs the RESET signal for the CPU and 32.768KHz clock signal for the CPU boot-up.



- The Multi Media CPU (IC9201) operates with main CPU clock "800MHz" oin the PLL circuit based on the 24MHz crystal oscillator (X'Tal). And with external IC connections, processing including USB function, wired LAN, wireless LAN images, image file playback, video signal output, and audio signal output are performed.
- □ The Main OS is "WindowsCE6.0 R3". The firmware is stored in the NAND Flash ROM (IC9402). Also, the user settings in the MM board in relation to networks are stored in IC9402.
- DDR2 SDRAM (IC9401, IC9403) operates with a 200MHz clock from the CPU, and the internal processing speed is 400MHz.
- □ The USB processing is connected with USB-PHY (IC9602) and ULPI. The signals are converted to USB signals (D+, D-), then to 3rd-circuit USB signals in USB-HUB (IC9604). The 1st circuit is used for the external USB terminal, 2nd circuit for the wireless LAN unit USB terminal, and 3rd circuit for the internal communications. In addition, Hi-Side SW (IC9605) is used as the overcurrent protection circuit for the VBUS of USB.
- □ The LAN processing is input to RJ-45 terminal connected with LAN-PHY (IC9603) and MII interface.
- □ Also, the CPU outputs the 8 bit R/G/B video signals to the MAIN board with the POMM2 connector.
- □ On CPU start-up, it is activated with a 32.768KHz clock signal output from PMIC (IC9008). And after the firmware is activated, the external 24MHz crystal oscillator (X'tal: X9201) is activated.
- □ The CPU is equipped with the following internal communication (communication among ICs or boards) in addition to the external terminal control.
  - > 1 circuit for internal I2C BUS communication.
  - > For communication with IC (IC9601) for RTC.
  - > 1 circuit for UART for internal communication.
  - > For communication with MAIN board.
  - > 1 circuit for USB OTG for internal communication.
  - > For communication with the CPU on the MAIN board (12Mbps).



- The Multi Media CPU (IC9201) and USB-PHY (IC9602) are connected with ULPI (UTMI+Low Pin Interface), an interface standard between the USB controller (logical layer circuit) and the transceiver (physical circuit).
- The USB-PHY is operated with a 24MHz clock signal from a crystal controlled oscillator (X9601). And the USB-HUB (IC9604) which is operated with a 30MHz crystal oscillator (X9602) splits into 3 circuits of I/O: USB signals for the external USB terminal, the wireless LAN unit terminal, and internal communication. The USB signals conform to USB2.0 (High-Speed/Full-Speed/Low-Speed). However, the signals for the internal communication operates in Full-Speed.
- □ The VBUS (+5V) of the USB terminal is controlled by the overcurrent protection circuit, Hi-Side SW (IC9605).
- The wireless LAN function is enabled by connecting a dedicated LAN unit with a USB-A terminal (exclusive use for wireless LAN) to achieve wireless communication with the PC. By using dedicated software, it is possible not only to control the projector, but also to display images from the PC to the projector.



- The Multi Media CPU (IC9201) and LAN-PHY (IC9603) are connected with IEEE802.3u standard, MII (Media Independent Interface), an interface between MAC layer and physical layer on the 10M,100M bit/sec CSMA/CD method LAN (Ethernet). The LAN PHY operates with a 25MHz crystal oscillator (X9603), and connects to the RJ-45 terminal (M9601) with a pulse transformer. The RJ-45 terminal has a built-in LED which indicates the operating state. It is controlled by LAN PHY.
  - > Green LED: Lit when 100Base is linked.
  - > Yellow LED: Off during data communication.
- □ The wired LAN function enables communication with the PC on the network by connection with the RJ-45 terminal.
- □ By using dedicated software, it is possible not only to control the projector, but also to display images from the PC to the projector.



- □ From the Multi Media CPU (IC9201), 8-bit RGB video signals, synchronized with Pixel Clock are output. Also, HD-Sync (horizontal sync signal) and VD-Sync (vertical sync signal) are output.
- □ The output video signal is processed in the MAIN board side via POMM2.



- □ The audio output from the MM board functions when audio is to be output in the video playback.
- The 24MHz-Clock signal from the crystal oscillator (X9601) is output to the MAIN board via POMM2 (70-pin connector). This 24MHz-Clock signal is input to the audio DAC (IC5202) of the MAIN board. The audio DAC outputs BCLK (Bit Clock) and WCLK (Word Clock) based on the 24MHz-Clock signal. This signal is input to the CPU (IC9201) via POMM2 (70-pin connector). The CPU (IC9201) outputs the audio data which is synchronized with BCLK (Bit Clock) and WCLK(Word Clock), then input to the audio DAC on the MAIN board. The analog audio signal which is converted to a digital signal is input to the audio processor IC (5401). The signal level of 24MHz-Clock/BCLK (Bit Clock) and WCLK (Word Clock)/audio data differ in the MM board side and the MAIN board side, therefore level conversion is processed with IC9003, IC9004, and IC9007. IC9003, IC9004, and IC9007 are controlled by the BUF\_EN signal (enable signal).



The RTC(Real Time Clock) keeps the date and time. It is used for program timer functions and checking the expiration date of the network certificate. To keep the time information while the projector power is OFF, it can operate with electric charge stored in the double-layer capacitor (EDLC:C9669). The standard clock is 32.768KHz (X9604), and communication with the CPU is achieved via I2C communication.



- □ The CPU (IC4001) is equipped with USB Host & Device, Ethernet MAC, I2C, UART, and GPIO. It controls the projector.
- □ The following are connected to the Local BUS of the CPU: firmware, factory-set data and 64Mbit Nor Flash ROM(IC4003) which stores initial data for each device, 128Mb SDRAM(IC4002) for the work memory, Gate Array TightCell2 (IC4502) for the feature expansion, and SyncDet (IC2801) via Buffer (IC4703).
- □ The main uses of the CPU are as follows:
  - I2C BUS x1: Used with audio processor IC, audio DAC, A/D Converter, D/A Converter, and SUB CPU.
  - > UART x3

0ch:Used with a PC control terminal 1ch: Communication with REON 1 (CPUREQ TxD/RxD) 2ch: Communication with REON 2 (REONREQ TxD/TxD)

- GPIO: Used for power control signal, reset signal, state checking, or switching operation of each device.
- USB Host & Device: Connected with Multi Media CPU for UPDATE and communications.
- Ethernet MAC: Not in use



- Tight Cell2 (IC4502) is a Gate Array which is equipped with the following features, including fan revolution detection, key matrix, LED control. Tight Cell2 is controlled by the CPU (IC4001) via Local BUS. The supported functions are as follows;
  - Fan revolution detection x8: Detects the revolution from the rotational frequency pulse signal which is output from the fan. "FAN STOP" is detected when the revolution falls below the specification.
  - > Key matrix control: The CPU regularly monitors the key array.
  - > General-purpose I/O: Used for lighting control of LEDs and each IC.
  - > UART x2:

1st circuit for communication with the lamp.

2nd circuit for communication with the Multi Media CPU.

- > PWM output: Used for beep generation.
- 2nd circuit MIX for remote control signal light receiving section: Detects the remote control header section of each signal of the light receiving section.
- Decoder circuit for remote controller: Decodes the received signals in accordance with the hardware.





- SUBCPU (IC5001) controls the power of PS-UNIT and the CPU (IC4001). When the power is supplied, 5V is supplied to the regulator IC (IC5002) from PS-UNIT, and 3.3V is generated for SUBCPU. This 3.3V activates SUBCPU, and the PS\_ON signal is input to PS-UNIT. This performance enables power output other than 5V. After that, the CPU is activated by CPU\_ON signal input to the DC-DC converter for the CPU.
- □ SUBCPU, as a substitute for the CPU, decodes the remote control's Power ON command, and detects the Power ON command from the PC and Power key input. In the normal Power ON state, it controls the IRIS motor driver IC(IC5005).



#### No additional notes



#### No additional notes



- □ Lamp on/off is controlled by High/Low of TightCell2 (IC4502)'s Port: LAMP\_PW signals. Also, the lit status monitoring and brightness are controlled by the serial communication function of TightCell2 (TXD\_LAMP/RXD\_LAMP). The LAMP\_PW signal's lighting control is enabled only when the status of lamp cover, bimetal, and fan are in the normal state. In other cases, TightCell2 will detect the mechanical signal (COVERDET/LAMPHOT/FANDET1~8) and turn off the lamp.
- □ Also, there is an ECO Mode and this controls the lamp power as follows:
  - > When ECO is OFF: Lamp power is 100%
  - > When ECO is ON: Lamp power is 80%



No additional notes




No additional notes

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No additional notes