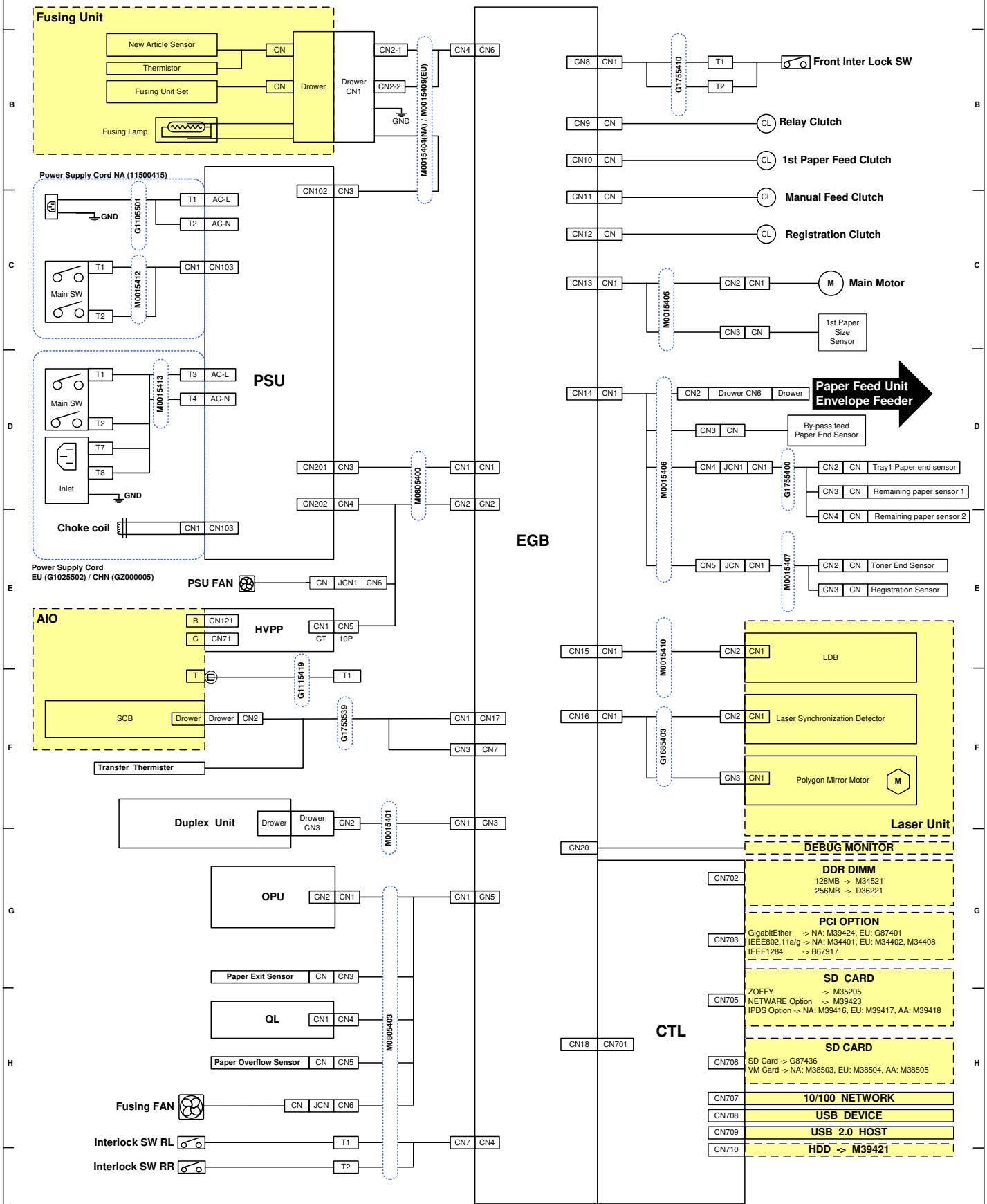
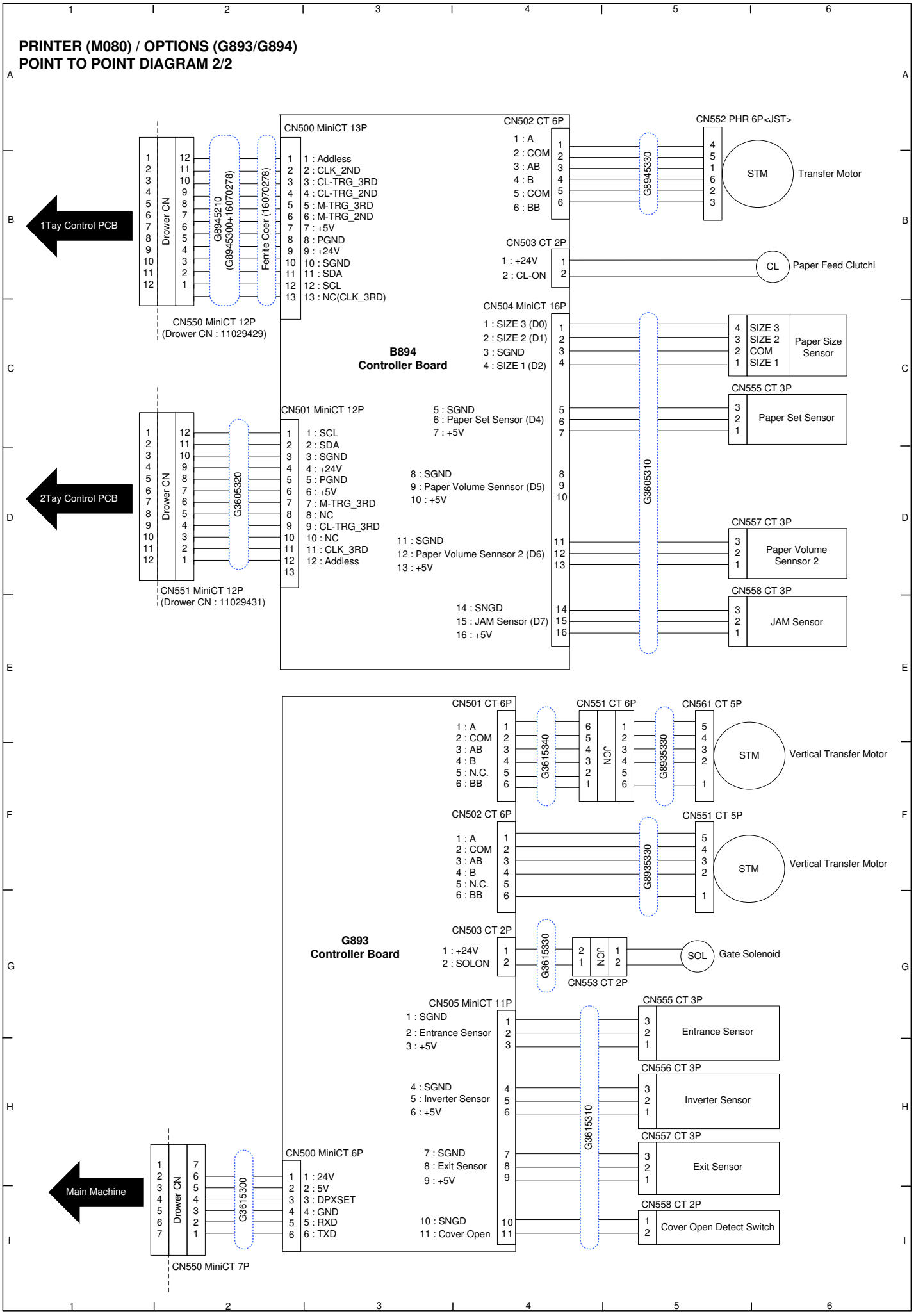
PRINTER (M080) / OPTIONS (G893/G894)
POINT TO POINT DIAGRAM 1/2



PRINTER (M080) / OPTIONS (G893/G894)
POINT TO POINT DIAGRAM 2/2



S-P3 Pin Assignment （EXP)

	Connector（FROM）			Signal information				Connector（TO）		
	No	Destination	Pin	Signal Name	Direction	L	H	No	Destination	Pin
M0015404	CN4	EGB CN6	1	Fusing Thermister	←	Analog Signal		CN2-1	Fusing Unit	4
			2	GND						3
			3	+5V						2
			4	Fusing New Unit Detection	←					1
			-	NC				CN2-2		4
			-	NC						3
			5	+5V						2
			6	Fusing New Unit Detection	←	Not set less than 0.5V				1
	T1	F.G.	Blown	F.G.				Drower		4
			-	NC						3
	CN3	PSU CN102	Bk	AC-L						2
			W	AC-N						1
M0015409	CN4	EGB CN6	1	Fusing Thermister	←	Analog Signal		CN2-1	Fusing Unit	4
			2	GND						3
			3	+5V						2
			4	Fusing New Unit Detection	←					1
			-	NC				CN2-2		4
			-	NC						3
			5	+5V						2
			6	Fusing Set Sensor	←	Not set less than 0.5V				1
	T1	F.G.	Red	F.G.				Drower		4
	CN3	PSU CN102	W	AC-N						3
			Bk	AC-L						2
			-	NC						1
M0805400	CN1	EGB CN1	1	GND				CN3	PSU CN201	1
			2	GND						2
			3	+24V						3
			4	+24V						4
			5	GND						5
			6	+5V						6
			7	+5VE		During power saving				7
	CN2	EGB CN2	1	PSU FAN Mortor	→	OFF	ON	CN6	PSU FAN	3
			2	PSU FAN Motor Lock Sensor	←	LOCK	Anomaly Detection			2
			3	GND						1
			4	Power Control Signal	→	Normal	Energy saving mode	CN4	PSU CN202	7
			5	Fusing Main Heater	→	OFF	ON			6
			6	Zero-cross interrupt established	←	Interrupt				5
			7	Fusing Relay Controrl	→	Relay ON	Relay OFF			4
			8	GND						3
			9	+24VS						2
			10	LD +5V						1
			11	Feedback Signal - ITB voltage	←			CN5	HVPP CN1	10
			12	Feedback Signal - Charged	←					9
			13	Feedback Signal - Development voltage	←					8
			14	ITB voltage（－）PWM Control	→					7
			15	ITB voltage（＋）PWM Control	→					6
			16	Charged voltage PWM Control	→					5
			17	Development voltage PWM	→					4
			18	+5V						3
			19	GND						2
			20	+24VS						1
G1755425	CN1	EGB CN17	1	GND				CN2	SCB	6
			2	SCB Set Sensor	←	Sset	Not Set			5
			3	SCB +5V						4
			4	I2C Serial Clock	→					3
			5	I2C Serial Data	←→					2
			6	GND						1
	CN3	EGB CN7	1	Transfer Thermistor Signal	←	Analog Signal			Trnsfer Thermistor	
			2	GND						
M0015401	CN1	EGB CN3	1	Duplex Unit Serial Recive Data	←			CN2	Duplex Unit	7
			2	Duplex Unit Serial Send Data	→					6
			3	GND						5
			4	Duplex Unit Set Sensor	←	Sset	No Set			4
			5	+5V						3
			6	+24V						2
			7	Fusing Relay	←	Relay ON	Relay OFF			1
G1755410	CN1	EGB CN8	1	+24VS		Front Cover / Rear Cover Paper Exit Cover During the open block		T1	Front Interlock Switch	
			2	NC						
			3	+24VSR		Rear Cover / Paper Exit Cover During the open block		T2		
	CN	EGB CN9	1	+24V					Middle Transfer Clutch	
			2	Clutch Control : Middle Transfer	→	ACT				
	CN	EGB CN10	1	+24V					1st Paper Feed Clutch	
			2	Clutch Control : 1st Paper Feed (Main)	→	ACT				
	CN	EGB CN11	1	+24V					By-pass Feed Clutch	
			2	Clutch Control : By-pass Feed	→	ACT				
	CN	EGB CN12	1	+24V					Resist Clutch	
			2	Clutch Control : Resistration	→	ACT				
M0015405	CN1	EGB CN13	1	Main Moter Clock	→			CN2	Msin Moter	8
			2	Brake Control : Main Motor	→	Active	Brake			7
			3	Clutch Control : Main Motor	→	ON	OFF			6
			4	Lock Sensor : Main Mortor	←	Lock	Anomaly Detection			5
			5	GND						4
			6	GND						3
			7	+24VS						2
			8	+24VS						1
			9	1st Paper Size Nob : Main	←			CN3	Paper Size Sensor : Main	4
			10	2st Paper Size Nob : Main	←					3
			11	GND						2
			12	3st Paper Size Nob : Main	←					1

	Connector（FROM）			Signal				Connector（TO）		
	No	Destination	Pin No	Signal Name	Direction	L	H	No	Destination	Pin No
M0805403	CN1	EGB CN5	1	COM1				CN2	Operation Panel CN2	7
			2	+5VE						6
			3	OPO_CLK						5
			4	+5VE						4
			5	OPO_CLK						3
			6	OPO_CLK						2
			7	COM1						1
			8	NC					NC	
			9	NC					NC	
			10	NC					NC	
			11	NC					NC	
			12	GND				CN4	Paper Exit Sensor	3
			13	Paper End Sensor Detect	←	In the paper	No paper			2
			14	+5V						1
			15	QL Control	→	ON	OFF	CN5	QL	2
			16	+24V						1
			17	GND				CN6	Paper Full Sensor - Paper Exit	3
			18	Detect Paper	←	In the paper	No paper			2
			19	+5V						1
			20	Fusing Fan	→	OFF	ON	CN7	Fusing FAN	3
			21	Detect Motor Lock :	←	Lock	Anomaly Detection			2
			22	GND						1
	CN8	EGB CN4	1	+24V				T1	Interlock Switch : Rear	1
			2	NC						
			3	+24VSR		Rear Cover / Paper Exit Cover During the open block		T2	Interlock Switch : Rear right	1
			4	NC						
			5	+24VSR2		Rear Cover During the open block		T1	Interlock Switch : Rear left	2
	T1	Inter Lock Switch : Rear	2	+24VSR2		Rear Cover During the open block		T2	Interlock Switch : Rear right	2
M0015406	CN1	EGB CN14	1	Bank Serial	→			CN2	500 Paper bank Emvelope bank（NA）	12
			2	Bank Serial	←→					11
			3	GND						10
			4	+24V						9
			5	GND						8
			6	+5V						7
			7	Motor Control :	→	ON	OFF			6
			8	Motor	→	ON	OFF			5
			9	Clutch	→	ON	OFF			4
			10	Clutch	→	ON	OFF			3
			11	Bank Motor	→					2
			12	GND						1
			13	GND				CN3	By-pass feed Paper End Sensor	3
			14	Detect Paper	←	In the paper	No paper			2
			15	+5V						1
			16	GND				G1755400 CN2	1Tray Paper End Sensor : Main	3
			17	Detect Paper	←	In the paper	No paper			2
			18	+5V						1
			19	GND				G1755400 CN3	1Tray Paper Near End Sensor : Main	3
			20	Detect Paper	←					2
			21	+5V						1
			22	GND				G1755400 CN4	1Tray Paper Near End Sensor 2 : Main	3
			23	Detect Paper	←					2
			24	+5V						1
			25	GND				M0015407 CN2	Toner End Sensor	4
			-	NC						3
			26	+5V						2
			27	Detection :	←	Toner End	There Toner			1
			28	GND				M0015407 CN3	Resist Sensor	3
			29	Detection :	←	In the paper	No paper			2
			30	+5V						1
M0015410	CN1	EGB CN15	1	LVDS Signal 2-	→			CN2	LDB	13
			2	LVDS Signal	→					12
			3	LVDS signal 1-	→					11
			4	LVDS Signal 1+	→					10
			5	GND						9
			6	APC Gate Signal 2	→	APC Active	APC Stop			8
			7	DAC Output	→	Analog Signal				7
			8	LD Error Detect Singal	←	Error	No Error			6
			9	DAC Output	→	Analog Signal				5
			10	GND						4
			11	LD Complete Off Signal	→	LD Clear off	LD Clear			3
			12	APC Gate	→	APC Active	APC Stop			2
			13	LD +5V						1
G1685403	CN1	EGB CN16	1	Synchronization Signal	←	Poligon Motor	No Synchronizati	CN2	Laser Synchronization Detector	3
			2	+5V						2
			3	GND						1
			4	Polygon Motor +24V				CN3	Polygon Motor	5
			5	GND						4
			6	Polygon	→	ON	OFF			3
			7	Polygon Motor Lock	←	Lock	Anomaly Detection			2
			8	Polygon	→					1
	T1	F.G.		F.G.				T2	F.G.	
		EGB CN20	1	+5V					Debug Monitor	
			2	Debug	←					
			3	Debug	→					
			4	GND						

	Connector（FROM）			Signal information				Connector（TO）		
	No	Destination	Pin No	Signal Name	Direction	L	H	No	Destination	Pin No
G1105501	T1	PSU	Bk	AC-L				Inlet	Cord	L
		F.G.		F.G.						F.G.
	T2	PSU	W	AC-N						N
M0015413	T3	PSU	Bk	AC-L				T1	MAIN SW	1
	T4		W	AC-N				T2		1
	T7	Inlet	Bk	AC-L				T1		2
	T8		W	AC-N				T2		2
M0015412	CN1	PSU CN103	1	AC-L				T1	MAIN SW	1
			2	AC-L						2
			3	AC-N				T2		1
			4	AC-N						2
G1115419	T1	HVPP		Transcriptional output				T2	AIO	
B TO B		EGB CN18	1	Energy saving mode Command	←				CTL CN701	1
			2	Horizontal sync	→					2
			3	GND						3
			4	LGATE Mode Vide	←					4
			5	GND						5
			6	Video Data 0	←					6
			7	Video Data 2	←					7
			8	Video Data 3	←					8
			9	Video Data 5	←					9
			10	Video Data 6	←					10
			11	Video Data 7	←					11
			12	Vertical Frame Gate	←					12
			13	Print Request	←					13
			14	Serial Recive Data	←					14
			15	Serial Send Data	→					15
			16	KEY Data Select Siganl 1	←	Enable				16
			17	Backlight signal	←	LED OFF	LED ON			17
			18	Alert LED Control	←	LED OFF	LED ON			18
			19	Data In LED Control	←	LED OFF	LED ON			19
			20	LCD/KEY Data Bus	←→					20
			21	LCD/KEY Data Bus 5	←→					21
			22	LCD/KEY DataBus	←→					22
			23	+3.3VE		During power saving				23
			24	Powr ON LED	←	LED ON	LED OFF			24
			25	On line LED Control	←	LED OFF	LED ON			25
			26	+5VE		During power saving				26
			27	+5VE		During power saving				27
			28	+5VE		During power saving				28
			29	+5VE		During power saving				29
			30	+5VE		During power saving				30
			31	Engine ready signal	→					31
			32	GND						32
			33	Gated horizontal	←					33
			34	GND						34
			35	GND						35
			36	GND						36
			37	Video Data 1	←					37
			38	GND						38
			39	Video Data 4	←					39
			40	GND						40
			41	Vertical Sync	→					41
			42	GND						42
			43	Printable State	→					43
			44	GND						44
			45	KEY Data Select	←	Enable				45
			46	GND						46
			47	GND						47
			48	GND						48
			49	LCD/KEY Data	←→					49
			50	GND						50
			51	GND						51
			52	LCD Data Wright Signal	←					52
			53	LCD Register Select Signal]	←	DATA	IR			53
			54	+5VE		During power saving				54
			55	Return signal : During power saving	←					55
			56	+5VE		During power saving				56
			57	MICR Toner Set LED Control	←					57
			58	+5VE		During power saving				58
			59	Buzer Signal	←					59
			60	+5VE		During power saving				60

	Connector（FROM）			Signal information				Connector（TO）		
	No	Destination	Pin	Signal Name	Direction	L	H	No	Destination	Pin
B TO B		CTL CN702	1	DQ0	←→				DDR DIMM 128MB Memory 256MB Memory	1
			2	Vss						2
			3	DQ1	←→					3
			4	DQS0	←→					4
			5	VDD						5
			6	DQ2	←→					6
			7	DQ3	←→					7
			8	VDD						8
			9	DQ8	←→					9
			10	DQ9	←→					10
			11	Vss						11
			12	DQS1	←→					12
			13	DQ10	←→					13
			14	VDD						14
			15	DQ11	←→					15
			16	VSS						16
			17	CK0	→					17
			18	CK0‾	→					18
			19	VDD						19
			20	CKE1	→	Clock Disable	Clock Enable			20
			21	A12	→					21
			22	NC						22
			23	A9	→					23
			24	A7	→					24
			25	Vss						25
			26	A5	→					26
			27	A3	→					27
			28	A1	→					28
			29	A10	→					29
			30	VDD						30
			31	BA0	→					31
			32	WE‾	→	Wright Enable	Wrighy Disable			32
			33	S0‾	→	Chip Select 0				33
			34	DQ16	←→					34
			35	Vss						35
			36	DQ17	←→					36
			37	DQS2	←→					37
			38	VDD						38
			39	DQ18	←→					39
			40	DQ19	←→					40
			41	VDD						41
			42	DQ24	←→					42
			43	DQ25	←→					43
			44	Vss						44
			45	DQS3	←→					45
			46	DQ26	←→					46
			47	Vss						47
			48	DQ27	←→					48
			49	SA0	→					49
			50	VREF						50
			51	DQ4	←→					51
			52	Vss						52
			53	DQ5	←→					53
			54	DM0	→					54
			55	VDD						55
			56	DQ6	←→					56
			57	DQ7	←→					57
			58	VDD						58
			59	DQ12	←→					59
			60	DQ13	←→					60
			61	Vss						61
			62	DM1	→					62
			63	DQ14	←→					63
			64	VDD						64
			65	DQ15	←→					65
			66	VSS						66
			67	CK1	→					67
			68	CK1‾	→					68
			69	VDD						69
			70	CKE0	←→	Clock Disable	Clock Enable			70
			71	A11	→					71
			72	A8	→					72
			73	A6	→					73
			74	A4	→					74
			75	VSS						75
			76	A2	→					76
			77	A0	→					77
			78	BA1	→					78
			79	RAS‾	→					79
			80	VDD						80
			81	CAS‾	→					81
			82	S1‾	→	Chip Select1				82
			83	A13	→					83
			84	DQ20	←→					84
			85	VSS						85
			86	DQ21	←→					86
			87	DM2	→					87
			88	VDD						88
			89	DQ22	←→					89
			90	DQ23	←→					90
			91	VDD						91
			92	DQ28	←→					92
			93	DQ29	←→					93
			94	VSS						94
			95	DM3	→					95
			96	DQ30	←→					96
			97	VSS						97
			98	DQ31	←→					98
			99	SDA	←→					99
			100	SCL	→					100

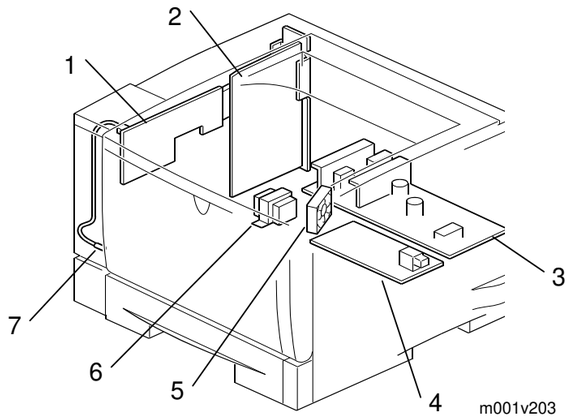
	Connector（FROM）			Signal information				Connector（TO）		
	No	Destination	Pin No	Signal Name	Direction	L	H	No	Destination	Pin No
B TO B		CTL CN703	1	GND					PCI Option Gigabit Ether IEEE802.11.a/g（NA） IEEE802.11.a/g(EU) IEEE802.11.g IEEE1284	1
			2	INT_N	←					2
			3	GND						3
			4	Reserved						4
			5	GND						5
			6	CLKRUN	→					6
			7	GND						7
			8	RST_N	→					8
			9	GND						9
			10	CLK	→					10
			11	GND						11
			12	GNT_N	→					12
			13	GND						13
			14	REQ_N	←					14
			15	GND						15
			16	PME_N	←					16
			17	AD31	←→					17
			18	AD30	←→					18
			19	AD29	←→					19
			20	AD28	←→					20
			21	AD27	←→					21
			22	AD26	←→					22
			23	GND						23
			24	AD25	←→					24
			25	AD24	←→					25
			26	C/BE3_N	←→					26
			27	GND						27
			28	IDSEL	→					28
			29	AD23	←→					29
			30	AD22	←→					30
			31	GND						31
			32	AD21	←→					32
			33	AD20	←→					33
			34	AD19	←→					34
			35	GND						35
			36	AD18	←→					36
			37	AD17	←→					37
			38	AD16	←→					38
			39	GND						39
			40	C/BE2_N	←→					40
			41	FRAME_N	←→					41
			42	IRDY_N	←→					42
			43	GND						43
			44	TRDY_N	←→					44
			45	DEVSEL_N	←→					45
			46	STOP_N	←→					46
			47	GND						47
			48	PERR_N	←→					48
			49	SERR_N	←→					49
			50	PAR	←→					50
			51	GND						51
			52	C/BE1_N	←→					52
			53	AD15	←→					53
			54	AD14	←→					54
			55	GND						55
			56	AD13	←→					56
			57	AD12	←→					57
			58	AD11	←→					58
			59	GND						59
			60	AD10	←→					60
			61	AD9	←→					61
			62	AD8	←→					62
			63	GND						63
			64	C/BE0_N	←→					64
			65	AD7	←→					65
			66	AD6	←→					66
			67	AD5	←→					67
			68	AD4	←→					68
			69	AD3	←→					69
			70	AD2	←→					70
			71	+3.3V						71
			72	AD1	←→					72
			73	+3.3V						73
			74	AD0	←→					74
			75	+3.3V						75
			76	Reserved						76
			77	+3.3V						77
			78	Reserved						78
			79	+3.3V						79
			80	Reserved						80
			81	+3.3V						81
			82	Reserved						82
			83	+5V						83
			84	Reserved						84
			85	+5V						85
			86	Reserved						86
			87	+3.3Vaux						87
			88	+24V						88

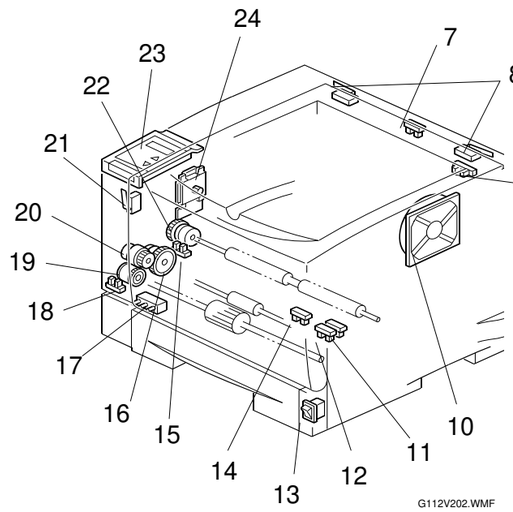
	Connector（FROM）			Signal information				Connector（TO）		
	No	Destination	Pin No	Signal Name	Direction	L	H	No	Destination	Pin No
B TO B		CTL CN705	1	Data	←→				SD Card ZOFFY NETWARE Option IPDS Option	1
			2	Command	←→					2
			3	GND						3
			4							4
			5	Clock	→					5
			6	GND						6
			7	Data	←→					7
			8	Data	←→					8
			9	Data	←→					9
B TO B		CTL CN706	1	Data	←→				SD Card VM Card（NA） VM Card（EU）	1
			2	Command	←→					2
			3	GND						3
			4							4
			5	Clock	→					5
			6	GND						6
			7	Data	←→					7
			8	Data	←→					8
			9	Data	←→					9
Extend I/F		CTL CN707	1	Recive Data(+)	←				10/100 NETWORK	1
			2	Recive Data(-)	←					2
			3	RCT						3
			4	TCT						4
			5	Send Data(+)	→					5
			6	Send Data(-)	→					6
			7	NC						7
			8	FG						8
Extend I/F		CTL CN708	1	+5VE					USB DEVICE	1
			2	Data-	←→					2
			3	Data+	←→					3
			4	GND						4
Extend I/F		CTL CN709	1	+5VE					USB HOST	1
			2	Data-	←→					2
			3	Data+	←→					3
			4	GND						4
B TO B		CTL CN710	1	+5VE		During power saving			HDD	1
			2	+5VE		During power saving				2
			3	Reserve						3
			4	FAN Control Signal	→		FAN ON			4
			5	HDD Power Detect	←		HDD Detect			5
			6	I2C Data	←→					6
			7	HDD Buffer Direction	→	HDD→CTL	CTL→HDD			7
			8	HDD DataBus	←→					8
			9	HDD DataBus	←→					9
			10	HDD DataBus	←→					10
			11	HDD DataBus	←→					11
			12	HDD DataBus	←→					12
			13	HDD DataBus	←→					13
			14	HDD DataBus	←→					14
			15	HDD DataBus	←→					15
			16	HDD DataBus	←→					16
			17	HDD DataBus	←→					17
			18	HDD DataBus	←→					18
			19	HDD Write Signal	→					19
			20	HDD Read Signal	→					20
			21	HDD Acknowledge	→					21
			22	Interrupt Request	←					22
			23	HDD Address Bus	→					23
			24	HDD Address Bus	→					24
			25	Chip Select 1	→					25
			26	+5VE		During power saving				26
			27	+5VE		During power saving				27
			28	GND						28
			29	HDD Control Signal	→	HDDON	OFF			29
			30	Power Control Signal	→	Controller ON	OFF			30
			31	GND						31
			32	I2C Clock	→					32
			33	HDD Reset Signal	→					33
			34	HDD Data Bus	←→					34
			35	GND						35
			36	HDD Data Bus	←→					36
			37	GND						37
			38	HDD Data Bus	←→					38
			39	NC						39
			40	HDD Data Bus	←→					40
			41	GND						41
			42	HDD Data Bus	←→					42
			43	GND						43
			44	HDD Data Request	←					44
			45	NC						45
			46	HDD IO Ready	←					46
			47	GND						47
			48	HDD Address Bus	→					48
			49	GND						49
			50	Chip Select 0	→					50
		CTL CN711	1	+5VE		During power saving			Debug	1
			2	Serial GND						2
			3	Serial GND						3
			4	Send Data0	→					4
			5	Recive Data0	←					5
			6	Send Data1	→					6
			7	Recive Data1	←					7
			8	NMI(Push-SW)	←					8
		CTL CN714	1	+5VE		During power saving			Debug	1
			2	Recive Data0	←					2
			3	Send Data0	→					3
			4	Signal GND						4

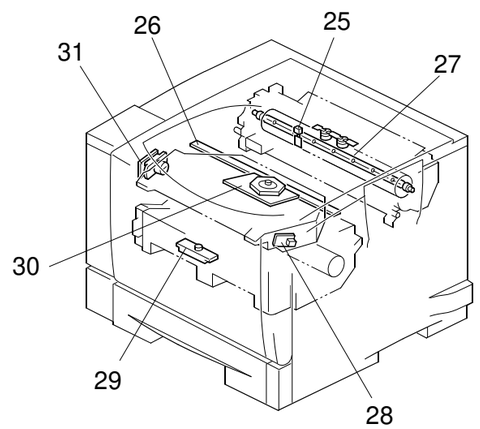
PRINTER (M008) / OPTIONS (G893/G894) ELECTRICAL COMPONENT LAYOUT

Printer (M080)

Symbol	Index No.	Description	P to P
Motors			
M1	24	Main	C6
M2	30	Polygonal Mirror	F6
M3	10	Exhaust Fan	H2
Magnetic Clutches			
MC1	16	Paper Feed	B5
MC2	20	Relay	B6
MC3	22	Registration	C5
MC4	19	By-pass feed	C5
Switches			
SW1	17	Paper Size	C6
SW2	21	Front Door	B6
SW3	8	Rear Cover	H2
SW4	13	Main	I2
Sensors			
S1	7	Paper Exit	G2
S2	9	Paper Overflow	H2
S3, S4	11, 12	Remaining Paper	DE-6
S5	14	Paper End	D6
S6	29	Toner End	E6
S7	15	Registration	E6
S8	18	By-pass paper	C6
PCBs			
PCB1	1	Engine	AI-4
PCB2	2	Printer controller	GI-4,5
PCB3	3	PSU (Power Supply Unit)	CDE2
PCB4	4	High Voltage Supply	E2
PCB5	31	LDD (Laser Diode Driver)	E6
PCB6	23	Operation Panel	G2
Lamps			
L1	27	Fusing	B1
L2	26	Quenching	H2
Others			
TH1	25	Fusing Thermistor	B1
TH2	7	Internal Thermistor	F1
LSD1	28	Laser Synchronization Detector	F6
CC1	6	Choke Coil (230V machine only)	E1
FAN1	5	PSU FAN	E2

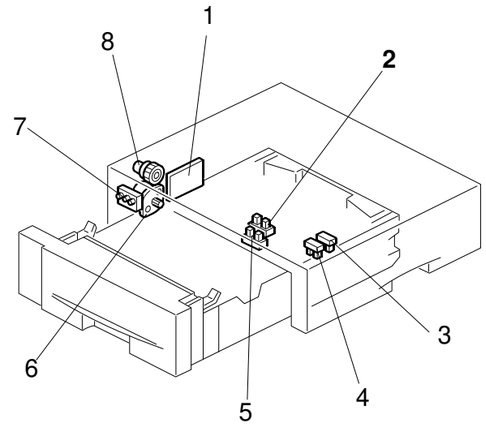






Paper Tray Unit (G894)

Symbol	Index No.	Description	P to P
Motor			
M1	6	Paper feed	B6
Sensors			
S1	5	Paper feed	C6
S2	2	Paper end	D6
S3, S4	3, 4	Remaining paper	E6
Switch			
SW1	7	Paper size	C6
Clutch			
MC1	8	Paper feed	B6
PCB			
PCB1	1	Paper tray board	A-E34



Duplex Unit (G893)

Symbol	Index No.	Description	P to P
Motor			
M1	8	Inverter	F6
M2	4		F6
Sensors			
S1	1	Entrance	G5
S2	5	Exit	H5
S3	6	Inverter	H5
Switch			
SW1	3	Cover	I5
Clutch			
SOL1	7	Inverter gate	G5
PCB			
PCB1	2	Duplex board	E-I,2-4