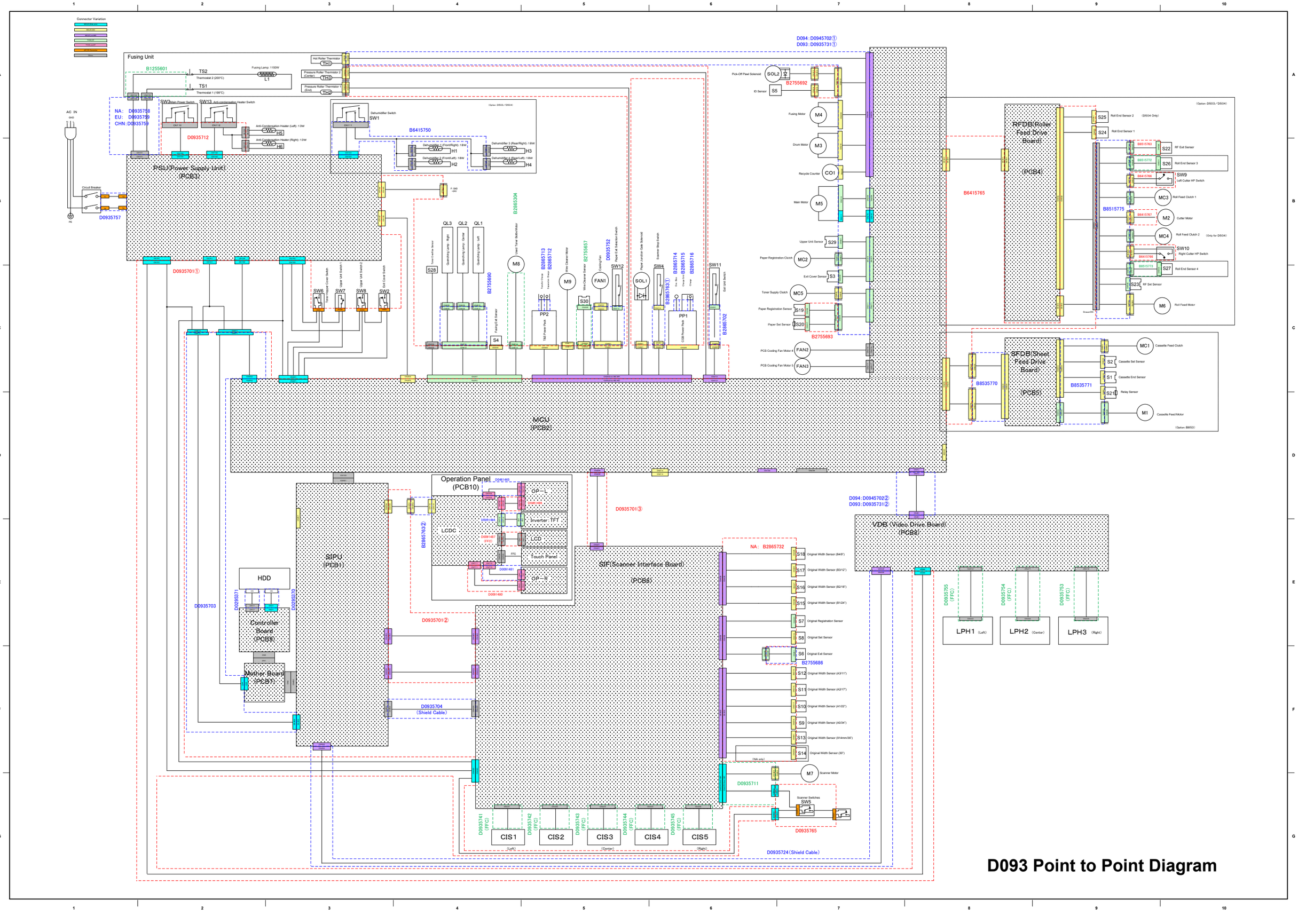


D093 Point to Point Diagram

CN102 : NVRAM [for Debug]

Pin_N O	Signal	Descripti on	I/ O	Action
1	A[31]	NVRAM : Data	O	—
2	A_[30]	NVRAM : Data	O	—
3	A[29]	NVRAM : Data	O	—
4	A[28]	NVRAM : Data	O	—
5	A[27]	NVRAM : Data	O	—
6	A[26]	NVRAM : Data	O	—
7	A[25]	NVRAM : Data	O	—
8	A[24]	NVRAM : Data	O	—
9	A[23]	NVRAM : Data	O	—
10	A[22]	NVRAM : Data	O	—
11	A[21]	NVRAM : Data	O	—
12	A[20]	NVRAM : Data	O	—
13	A[19]	NVRAM : Data	O	—
14	A[18]	NVRAM : Data	O	—
15	A[17]	NVRAM : Data	O	—
16	NC	-	—	—
17	RD_N	NVRAM : Read	O	—
18	WR_N	NVRAM : Write	O	—
19	NVRAM_CS_N	NVRAM : CS	O	—
20	-	-	—	—
21	D[0]	NVRAM : Address	B	—
22	D[1]	NVRAM : Address	B	—
23	D[2]	NVRAM : Address	B	—
24	D[3]	NVRAM : Address	B	—
25	D[4]	NVRAM : Address	B	—
26	D[5]	NVRAM : Address	B	—
27	D[6]	NVRAM : Address	B	—
28	D[7]	NVRAM : Address	B	—
29	3.3V	-	P	—
30	3.3V	-	P	—
31	5V	-	P	—
32	5V	-	P	—
33	5V	-	P	—

CN102 : NVRAM [for Debug]

Pin_N O	Signal	Descripti on	I/ O	Action
1	A[31]	NVRAM : Data	O	—
2	A_[30]	NVRAM : Data	O	—
3	A[29]	NVRAM : Data	O	—
4	A[28]	NVRAM : Data	O	—
5	A[27]	NVRAM : Data	O	—
6	A[26]	NVRAM : Data	O	—
7	A[25]	NVRAM : Data	O	—
8	A[24]	NVRAM : Data	O	—
9	A[23]	NVRAM : Data	O	—
10	A[22]	NVRAM : Data	O	—
11	A[21]	NVRAM : Data	O	—
12	A[20]	NVRAM : Data	O	—
13	A[19]	NVRAM : Data	O	—
14	A[18]	NVRAM : Data	O	—
15	A[17]	NVRAM : Data	O	—
16	NC	-	—	—
17	RD_N	NVRAM : Read	O	—
18	WR_N	NVRAM : Write	O	—
19	NVRAM_CS_N	NVRAM : CS	O	—
20	-	-	—	—
21	D[0]	NVRAM : Address	B	—
22	D[1]	NVRAM : Address	B	—
23	D[2]	NVRAM : Address	B	—
24	D[3]	NVRAM : Address	B	—
25	D[4]	NVRAM : Address	B	—
26	D[5]	NVRAM : Address	B	—
27	D[6]	NVRAM : Address	B	—
28	D[7]	NVRAM : Address	B	—
29	3.3V	-	P	—
30	3.3V	-	P	—
31	5V	-	P	—
32	5V	-	P	—
33	5V	-	P	—

CN121: IC Card

Pin_ No	Signal	Description	I/O	Action
1	GND	-	G	-
2	D[12]	IC_ CARD : Data	B	-
3	SWAP_N	IC_ CARD : SWAP	I	-
4	D[11]	IC_ CARD : Data	B	-
5	D[4]	IC_ CARD : Data	B	-
6	D[10]	IC_ CARD : Data	B	-
7	D[3]	IC_ CARD : Data	B	-
8	D[9]	IC_ CARD : Data	B	-
9	D[2]	IC_ CARD : Data	B	-
10	D[8]	IC_ CARD : Data	B	-
11	D[1]	IC_ CARD : Data	B	-
12	D[0]	IC_ CARD : Data	B	-
13	A[21]	IC_ CARD : Address	O	-
14	GND	-	G	-
15	CARD_ CS_ N	IC_ CARD : CS	O	-
16	A[20]	IC_ CARD : Address	O	-
17	A[22]	IC_ CARD : Address	O	-
18	A[23]	IC_ CARD : Address	O	-
19	A[14]	IC_ CARD : Address	O	-
20	A[18]	IC_ CARD : Address	O	-
21	A[13]	IC_ CARD : Address	O	-
22	A[17]	IC_ CARD : Address	O	-
23	A[12]	IC_ CARD : Address	O	-
24	A[10]	IC_ CARD : Address	O	-
25	A[11]	IC_ CARD : Ad dress	O	-
26	5V	IC_ CARD :	P	-
27	A[15]	IC_ CARD : Address	O	-
28	A[16]	IC_ CARD : Address	O	-
29	A[19]	IC_ CARD : Address	O	-
30	A[24]	IC_ CARD : Address	O	-
31	A[25]	IC_ CARD : Address	O	-
32	A[26]	IC_ CARD : Address	O	-
33	A[27]	IC_ CARD : Address	O	-
34	A[28]	IC_ CARD : Address	O	-
35	A[29]	IC_ CARD : Address	O	-
36	A[30]	IC_ CARD : Address	O	-
37	3.3V	-	P	-
38	D[15]	IC_ CARD : Data	B	-
39	D[7]	IC_ CARD : Data	B	-
40	D[14]	IC_ CARD : Data	B	-
41	D[6]	IC_CA RD : Data	B	-
42	D[13]	IC_ CARD : Data	B	-
43	D[5]	IC_ CARD : Data	B	-
44	CARD_ SET_ N	IC_ CARD : SET	I	H : Not set L : Card set
45	GND	-	G	-

CN122: Emulator

Pin_No	Sign al	Description	I/ O	Action
1	SDI_RSRT_N	Emulator	I	-
2	3.3V	-	P	-
3	3.3V	-	P	-
4	TDO	Emulator	O	-
5	TCK	Emulator	I	-
6	GND	-	G	-
7	TMS	Emulator	I	-
8	TDI	Emulator	I	-
9	GND	-	G	-
10	TRS	Emulator	I	-
11	DBI	Emulator	I	-
12	GND	-	G	-
13	TRSYNC	Emulator	O	-
14	GND	-	G	-
15	TRCLK	Emulator	O	-
16	GND	-	G	-
17	TRDATA0	Emulator	O	-
18	GND	-	G	-
19	TRDATA1	Emulator	O	-
20	GND	-	G	-
21	TRDATA2	Emulator	O	-
22	GND	-	G	-
23	TRDATA3	Emulator	O	-
24	GND	-	G	-
25	TRDATA4	Emulator	O	-
26	GND	-	G	-
27	TRDATA5	Emulator	O	-
28	GND	-	G	-
29	TRDATA6	Emulator	O	-
30	GND	-	G	-
31	TRDATA7	Emulator	O	-
32	GND	-	G	-
33	EVENT0	Emulator	O	-
34	GND	-	G	-
35	EVENT1	Emulator	O	-
36	SDI_3	Emulator	I	-

CN130: Debug Monitor

Pin_ No	Signal	Description	I/O	Action
1	5V	Debug Monitor	P	-
2	DEBUG_ T XD	Debug Monitor	O	-
3	DEBUG_ R XD	Debug Monitor	I	-
4	GND	Debug Monitor	G	-

CN132: Folder Unit

Pin_No.	Signal	Description	I/O	Action
A1	GND	-	G	-
A2	G ND	-	G	-
A3	G ND	-	G	-
A4	G ND	-	G	-
A5	G ND	-	G	-
A6	G ND	-	G	-
A7	G ND	-	G	-
A8	G ND	-	G	-
B1	UARTRXD_B	Folder _RX	I	-
B2	UARTRXD_A	-- "	I	-
B3	UARTTXD_Z	Folder _TX	O	-
B4	UARTTXD_Y	-- "	O	-
B5	ORI_+5V_SENS	Folder PW: OFF/ON Detection	I	H : OFF L : ON
B6	+5V_NEP	Main PW : FFION Out	O	H : OFF L : ON
B7	G ND	-	G	-
B8	SENS_ORI	Connection Check: Folder	I	H : Not connected L : Connected

CN133: SIPU I/F (1 – 20 Pins)

Pin_No	Signal	Description	I/O	Action
1	--	-	--	--
2	+24V	OPU Power (Backlight)	P	-
3	GND	-	G	-
4	VDB_LSYNC	VDBLSYNC	O	H : Disabled L : Enabled
5	GND	-	G	-
6	ICIB_SET	ICIB_ Set Detection	I	-
7	FUNK I	Energy Saver	I	-
8	SLEEP_OFF	SLEP_N	O	-
9	BCU_STATE	3.3V pukup Fixed	I	-
10	GND	-	G	-
11	En er gy - Save	Power Off Signal	I	-
12	GND	-	G	-
13	DEVE_P31	Judge 0	I	-
14	DEV1_P31	Judge 1	I	-
15	DEV2_P31	Judge 2	I	-
16	GND	-	G	-
17	DONE_Cetus	Conf i g Completed - Cetus	I	-
18	DONE_S I B	Conf i g Completed - S I B	I	-
19	nCONF I G_Cetus	Conf i g Start - Cetus	O	-
20	n CONF I G_S I B	Conf i g Start - S I B	O	-

CN133: SIPU I/F (21 – 60 Pins)

P1 n_No	Signal	Description	I /O	Action
21	XSFGATE	Scanner FGATE	O	-
22	GND	-	G	-
23	I PU_CLKO	P ull down	O	-
24	I PU_RD_N	-	O	-
25	I PU_WR_N	-	O	-
26	I PU_RESET_N	-	O	-
27	I PU_CS_N5	CETUS	O	-
28	I PU_CS_N4	IRI 2001(DEVO - 4)	O	-
29	I PU_CS_N3	I C I B IRI 2001A	O	-
30	I PU_CS_N2	LUPUS	O	-
31	I PU_CS_N1	S I B (IRI - 2005)	O	-
32	I PU_CS_N0	S I B (FPGA)	O	-
33	GND	-	G	-
34	I PU_I N72_N	Reserve	I	-
35	I PU_I N71_N	CETUS	I	-
36	I PU_I N70_N	LUPUS	I	-
37	GND	-	G	-
38	GND	-	G	-
39	GND	-	G	-
40	NC	-	-	-
41	-	-	-	-
42	-	-	-	-
43	+24 V	OP U Power (Backlight)	P	-
44	GND	-	G	-
45	GND	-	G	-
46	I PU_DATA(15)	I PU Data Bus	B	-
47	I PU_DATA(14)	I PU Data Bus	B	-
48	I PU_DATA(13)	I PU Data Bus	B	-
49	I PU_DATA(12)	I PU Data Bus	B	-
50	I PU_DATA(11)	I PU Data Bus	B	-
51	I PU_DATA(10)	IPU Data Bus	B	-
52	I PU_DATA(9)	IPU Data Bus	B	-
53	I PU_DATA(8)	IPU Data Bus	B	-
54	I PU_DATA(7)	IPU Data Bus	B	-
55	I PU_DATA(6)	IPU Data Bus	B	-
56	I PU_DATA(5)	IPU Data Bus	B	-
57	I PU_DATA(4)	IPU Data Bus	B	-
58	I PU_DATA(3)	IPU Data Bus	B	-
59	I PU_DATA(2)	IPU Data Bus	B	-
60	I PU_DATA(1)	IPU Data Bus	B	-

CN202: PSU Signal

Pin_No	Signal	Description	I/O	Action
1	5Vs	-	P	-
2	GND	-	G	-
3	5V	-	P	-
4	GND	-	G	-
5	12V	-	P	-
6	GND	-	G	-
7	12V	-	P	-
8	GND	-	G	-

CN202: PSU Control

Pin_No	Signal	Description	I/O	Action
1	24VinN2	PSU Fan	P	-
2	GND	-	G	-
3	PSU_FAN	PSU Fan	O	H : OFF L : ON
4	TRG_SW_ENG	-	O	H : OFF L : ON
5	2RLY_PSU	Power Relay	G	H : OFF L : ON
6	XINT_ZERO	Zero Cross Interruption	I	-
7	TRG_FUS	Fusing Lamp TRG1	O	H : ON L : OFF
8	TRG_HUMID	Dehumidifier	O	H : ON L : OFF

CN206: Scanner Drive

Pin_ N o	Signal	Description	I / O	Action
1	S_RGST_DOC	Original Registration Sensor	I	H : Paper detected L : Not detected
2	IREF_CSAN	Scanner Moto r: P W SW	O	H : Normal L : P W reduction
3	GND	GND	G	-
4	XRES_SCAN	Scanner Moto r: RESET	O	H : Normal L : Reset
5	XENB_SCAN	Scanner Moto r: ENABLE	O	H : Normal L : PW cut
6	GND	GND	G	-
7	CCW_SCAN	Scanner Moto r: CW/CCW	O	H : Reverse L : Forward
8	TPU_SCAN	Scanner Moto r: CLOCK	O	-
9	MO1	Scanner Moto r: MODE1	O	H : 2W1 - 2 phase L : W1 - 2 phase

CN204: PP, Cooling Fan, CCM

Pin_No	Signal	Description	I/O	Action
A1	ERR_DDC	PP D Erro r Detection	I	-
A2	TRG_PP_DDC	PP_Separation_DC_TRG	O	H : O FF L : O N
A3	P P _DAC_STR	Se p arat io n AC Start S ig nal	O	H : O FF L : O N
A4	TR G _P P _DAC	P P _ Se p arat io n_ A C_TR G	O	H : O FF L : O N
A5	FB_T	P P T Return	I	-
A6	TR G _ P P _T	P P _Transfer_TR G	O	H : O FF L : O N
A7	G ND	-	G	-
A8	G ND	-	G	-
A9	24V / -N2	P P - C G B	P	-
A10	CLE_M_CC -	Wi re Cleaner Motor (-)	O	H : CCW L : CW
A11	CLE_M_CC+	Wi re Cleaner Motor (+)	O	H : CW L : CCW
A12	24V / -N2	C ooling Fan Dri ve	P	-
A13	2FAN1	C ooling Fan Dri ve	O	H : O FF L : O N
A14	XL O K_FAN	C ooling Fan Lo ck	I	H : L O CK L : UN L O CK
A15	S P D_FAN	N o t used	-	-
A16	5V	-	P	-
A17	CLM_L O K	Wi re Cleaner Motor Lo ck	I	H : L O CK L : UN L O CK
A18	G ND	-	G	-
B1	FB_B	P P B Return	I	-
B2	TR G _ P P _B	P P _ Bi as _TR G	O	H : O FF L : O N
B3	G ND	-	G	-
B4	TR G _ P P _ G	P P _ Grid _TR G	O	H : O FF L : O N
B5	ERR_C	PP C Error Detection	I	-
B6	TRG_PP_C	PP_Charger_TRG	O	H : O FF L : O N
B7	GND	-	G	—
B8	GND	-	G	—
B9	24V INT 1	PP - TO	P	—
B10	KEY_ST OP	Scanner Stop Key	I	H : O FF L : O N
B11	GND	-	G	—
B12	24VinN2	Paper Junction Gate S O L	P	—
B13	S O L_ O P_HAI	Paper Junction Gate S O L	O	H : O FF L : O N
B14	GND	-	G	—
B15	NC	-	—	—
B16	FB_FU2	Pressure Roller Thermistor	I	—
B17	SW_DGID_HAI	Paper Exit Selection Switc h	I	—
B18	GND	Paper Exit Selection Switc h	P	—

CN207: Quenching Lamp, Used Toner

Pin_ N o	Signal	Description	I/O	Action
1	GND	-	G	-
2	S_TONER_OF	Toner Overflow Sensor	I	H : Not full L : Full
3	5V	-	P	-
4	24VinN2	Quenc hing Lamp	P	-
5	TRG_Q_R	Quenc hing Lamp Right _ TRG	O	H : OFF L : ON
6	24VinN2	Quenc hing Lamp	P	-
7	TRG_Q_C	Quenc hing Lamp Center _TRG	O	H : OFF L : ON
8	24VinN2	Quenc hing Lamp	P	-
9	TRG_Q_L	Quenc hing Lamp Left _TRG	O	H : OFF L : ON
10	GND	-	G	-
11	S_HAISI	Fusing Exit Sensor	I	H : Not detected L : Paper detected
12	5V	-	P	-
13	24 V3	Used Toner Bottle Motor	P	-
14	1BOT_M	Used Toner Bottle Motor	O	H : ON L : OFF

CN208: Fusing, Paper Exit, Drum Motor and ID Sensor

Pin_ N o.	Signal	Description	I/ O	Action
A1	SOL_BUNRI	Pick - Off Pawl Solenoi d	O	H : OFF L : ON
A2	24VinN2	Pick - Off Pawl Solenoi d	P	-
A3	12V	ID Sensor r	P	-
A4	TRG_PSEN	ID Sensor r	O	H : OFF L : ON
A5	FB_PS	ID Sensor r FB	I	-
A6	GND	-	G	-
A7	5V	-	P	-
A8	TPU_DC_M2	Fusing Motor _ Clock	O	-
A9	CCW_M2	Fusing Motor _CW/CCW	O	H : CW L : CCW
A10	LOK_M2	Fusing Motor _ Lock	I	H : Un lock L : Lock
A11	XTRG_M2	Fusing Motor _ TRG	O	H : OFF L : ON
A12	GND	-	G	-
A13	GND	-	G	-
A14	24VinN2	Fusing Motor	P	-
A15	24VinN2	Fusing Motor	P	-
A16	GND	-	G	-
A17	NC	-	I	-
B1	5V	-	P	-
B2	GND	-	G	-
B3	5V	-	P	-
B4	TPU_DC_DM	Drum Motor _ Clock	O	-
B5	CCW_DM	Drum Motor _CW/CCW	O	H : CW L : CCW
B6	LOK_DM	Drum Motor _ Lock	I	H : Un lock L : Lock
B7	XTRG_DM	Drum Motor _ TRG	O	H : OFF L : ON
B8	GND	-	G	-
B9	GND	-	G	-
B10	24VINT1	Drum Motor	P	-
B11	24VINT1	Drum Motor	P	-
B12	GND	-	G	-
B13	FB_FU1	Hot R ller Thermistor	I	-
B14	CNT_TOTAL	Recycle Counter Count	O	H : OFF L : ON
B15	24V3	Recycle Counter	P	-
B16	NC	-	-	-
B17	NC	-	-	-

CN209: Main Motor Signal

Pin_ N o.	Signal	Description	I/ O	Action
1	5V	-	P	-
2	GND	-	G	-
3	XTRG_M1	Main Motor _ TRG	O	H : OFF L : ON
4	CCW_M1	Main Motor _CW/CCW	O	H : CW L : CCW
5	TPU_DC_M1	Main Motor _ Clock	I	-
6	XLOK_M1	Main Motor _ Lock	O	H : Unlock L : Lock

CN210: Drum Motor Power

Pin_ N o.	Signal	Description	I/ O	Action
1	24VINT1	24V Drum M PW	P	-
2	GND	24 V - Drum M PW	G	-

CN211: Toner Supply CL, Model SW, Paper Registration Sensor, Paper Set SN

Pin_ N o.	Signal	Description	I/ O	Action
1	24VINT1	Toner Supply Clutc h	P	-
2	CL_TONER	Toner Supply Clutc h	P	H : OFF L : ON
3	Main - C2A1	Model a/b Detect 1	I	H : Model b L : Model a
4	Main - C2A2	Reserve Model a/b Detect 2	I	Not used
5	GND	-	G	-
6	GND	-	G	-
7	GND	-	G	-
8	S_RGST	Paper Registration Senso r	I	H : Not detec t ed L : Paper detected
9	5V	-	P	-
10	GND	-	G	-
11	S_HANDIN	Paper Set Senso r	I	H : Not detec t ed L : Paper detected
12	5V	-	P	-

CN212: VDB

Pin_ N o.	Signal	Description	I / O	Action
1	RESET_VDB	RESET_VDB	O	-
2	SYHS_N_VDB	SYHS_N_VDB	O	-
3	SYOUT_VDB	SYOUT_VDB	O	-
4	SYIN_VDB	SYIN_VDB	O	-
5	CLKSY_VDB	CLKSY_VDB	O	-
6	SYCS_N_VDB	SYCS_N_VDB	O	-
7	LSYC_VDB	-	I	-
8	GND	-	G	-
9	NC	-	-	-
10	NC	-	-	-

CN213: Exit Unit SW, Pressure Roller TH 2

Pin_ N o.	Signal	Description	I / O	Action
1	OPN_TEICHAKU	Exit Unit Switc h	I	H : OPEN L : CLOSE
2	GND	-	G	-
3	FB_FU3	P ressure Roller T H 2	I	-
4	GND	-	G	-

CN214: PCB FAN 1

Pin_ N o.	Signal	Description	I / O	Action
1	PCB_FAN123 _DC	24/12V	P	—
2	PCB_FAN123	PCB_ FAN 3	O	H : OFF L : ON
3	PCB_ FAN 45 _DC	24/12V	P	—
4	PCB_ FAN 45	PCB_FAN 4	O	H : OFF L : ON
5	PCB_ FAN 45 _DC	24/12V	P	—
6	PCB_ FAN 45	PCB_FAN 5	O	H : OFF L : ON

CN215: Upper Unit Sensor, Exit Cover Sensor, Paper Registration Clutch

Pin_ N o.	Signal	Description	I / O	Action
1	GND	-	G	-
2	S_OPN_L	Upper Unit Sensor	I	H : CLOSE L : O PEN
3	5V	-	P	-
4	24VINT1	Paper Registration Clutch	P	-
5	CL_RGST	Paper Registration Clutch	O	H : OFF L : O N
6	GND	-	G	-
7	S_OPN_HAI	Exit Cover Sensor	I	H : OPEN L : CLOSE
8	5V	-	P	-

CN216: PCB FAN2

Pin_ N o.	Signal	Description	I / O	Action
1	PCB_ FAN 123 _DC	24/12V	P	-
2	PCB_FAN1234_TRG	PCB_FAN13-1	O	H : OFF L : ON

CN216: PCB FAN3

Pin_ N o.	Signal	Description	I / O	Action
1	PCB_ FAN 123 _DC	24/12V	P	-
2	PCB_FAN1235_TR	PCB_FAN3-2	O	H : OFF L : ON

CN220: Roll Feeder

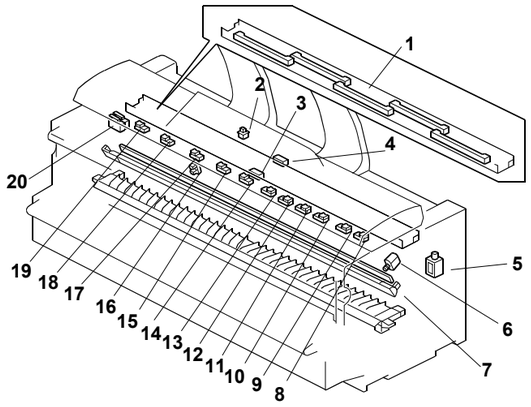
Pin No	Signal	Description	I/O	Action
A1	5V	-	P	—
A2	5V	-	P	—
A3	GND	-	G	—
A4	GND	-	G	—
A5	24V 3	Roll Feeder	P	—
A6	24V3	Roll Feeder	P	—
A7	24V3	Roll Feeder	P	—
A8	24V3	Roll Feeder	P	—
A9	24V3	Roll Feeder	P	—
A10	24V3	Roll Feeder	P	—
A11	SENS_RF1	Roll Feed 1 Set	I	H : Not connected d L : Connected
A12	SENS_RF2	Roll Feed 2 Set	I	H : Not connected L : Connected
A13	GND	-	G	—
A14	GND	-	G	—
A15	GND	-	G	—
A16	GND	-	G	—
A17	S_RF_RE1	Roll End SN 1	I	H : Paper detected L : Not detected
B1	S_RF_RE2	Roll End SN 2	I	H : Paper detected L : Not detected
B2	S_RF_EXI	RF Exit SN	I	H : Paper detected L : Not detected
B3	S_RF_OPN	RF Set SN	I	H : OPEN L : CLOSE
B4	S_RL_IN1	Roll End SN 3	I	H : Paper detected L : Not detected
B5	S_RL_IN2	Roll End SN 4	I	H : Paper detected L : Not detected
B6	CUT_HP_L	Left Cutter HP SW	I	H : Not HP L : HP
B7	CUT_HP_R	Right Cutter HP SW	I	H : Not HP L : HP
B8	CL_RF_1	Roll Feed CL 1	O	H : OFF L : ON
B9	CL_RF_2	Roll Feed CL 2	O	H : OFF L : ON
B10	GND	-	G	—
B11	XRST_M_RF	Roll Feed Motor _ Reset	O	H : Not reset L : Reset
B12	XENB_M_RF	Roll Feed Motor_Enable	O	H : ON L : OFF
B13	XMODE_M_RF	Roll Feed Motor _ Mode	O	H : ON L : OFF
B14	XCLK_M_RF	Roll Feed Motor _ Clock	O	-
B15	CWB_M_RF	Roll Feed Motor _CW/CCW	O	H : CW L : CCW
B16	CUT_M+	Roll Feed Motor (+)	O	H : CW L : CCW
B17	CUT_M -	Roll Feed Motor (-)	O	H : CCW L : CW

CN221: Cassette Feed Unit

Pin No.	Signal	Description	I/O	Action
A1	5V	-	P	-
A2	5V	-	P	-
A3	SENS_SF	Cassette Feed Connection	I	H : Not connected L : Connected
A4	GND	-	G	-
A5	24V3	Cassette Feed Connection	P	-
A6	24V3	Cassette Feed Connection	P	-
A7	GND	-	G	-
A8	GND	-	G	-
B1	S_SF_OPN	Cassette Set Sensor	I	H : CLOSE L : OPEN
B2	S_SF_JAM	Relay Sensor	I	H : No JAM L : JAM
B3	S_SF_PE	Cassette End Sensor	I	H : Not detected L : Paper detected
B4	CL_SF	Cassette Feed Clutch	O	H : OFF L : ON
B5	XENB_M_SF	Cassette Feed Motor_Enable	O	H : ON L : OFF
B6	XRST_M_SF	Cassette Feed Motor_Reset	O	H : - L : Reset
B7	XCLK_M_SF	Cassette Feed Motor_Clock Out	O	-
B8	XMODE_M_SF	Cassette Feed Motor_Mode	O	-

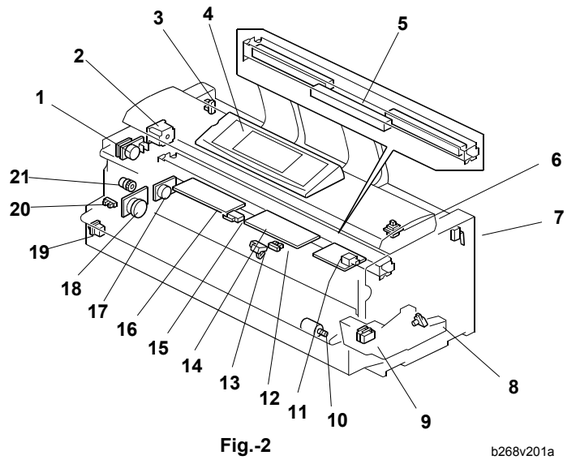
D093 Harness Pin Assignment List

D093 ELECTRICAL COMPONENT LAYOUT



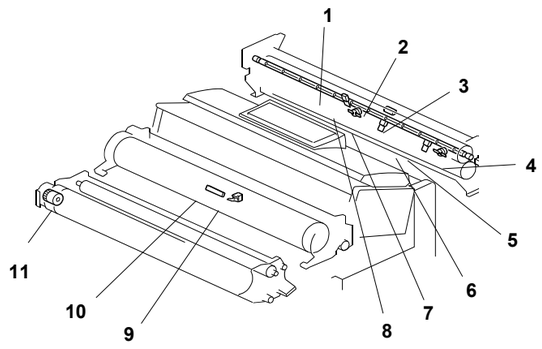
d093v202

Fig.-1



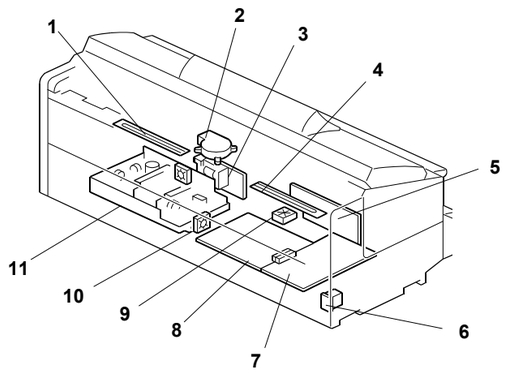
b268v201a

Fig.-2



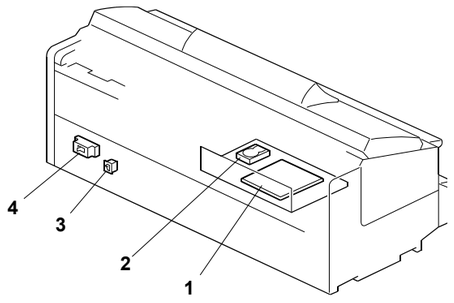
b268v203

Fig.-3



d093v204

Fig.-4



d093v205a

Fig.-5

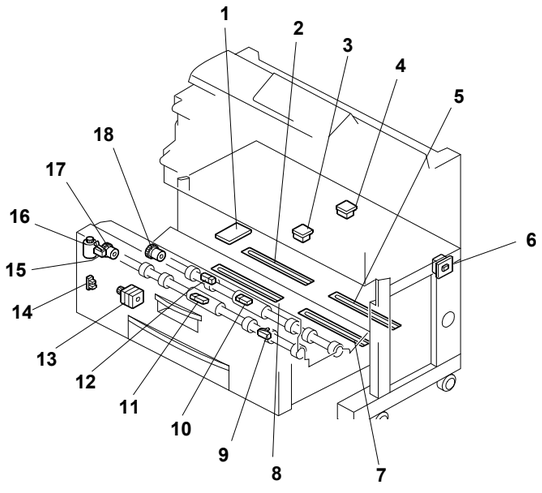


Fig.-6

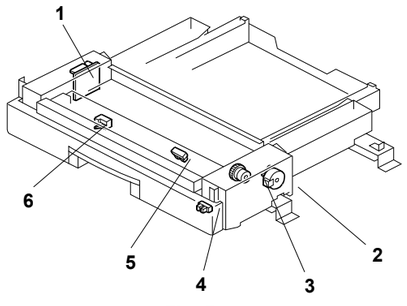


Fig.-7

Symbol	Index No.	Description	P to P
CIS			
CIS1 - 5	Fig.1-1	CIS 1 - 5 (Contact Image Sensor)	G4-6
LPH			
LPH1 - 3	Fig.2-5	LPH1 - 3 (LED Print Head)	E8-9
Lamp			
L1	Fig.3-3	Fusing Lamp	A2
Magnetic Clutches			
MC1	Fig.7-3	Cassette Feed Clutch	D9
MC2	Fig.2-21	Paper Registration Clutch	B7
MC3	Fig.6-17	Roll Feed Clutch 1	B10
MC4	Fig.6-18	Roll Feed Clutch 2	B10
MC5	Fig.3-11	Toner Supply Clutch	C7
Motors			
M1	Fig.7-2	Cassette Feed Motor	D9
M2	Fig.6-16	Cutter Motor	B10
M3	Fig.2-1	Drum Motor	B7
M4	Fig.2-17	Fusing Motor	A7
M5	Fig.2-18	Main Motor	B7
M6	Fig.6-13	Roll Feed Motor	C10
M7	Fig.2-2	Scanner Motor	G7
M8	Fig.2-10	Used Toner Bottle Motor	C4
M9	Fig.1-5	Wire Cleaner Motor	C5
Fans			
FAN1	Fig.4-2	Cooling Fan	C5
FAN2	Fig.4-10	PCB Cooling Fan Motor 4	C7
FAN3	Fig.4-9	PCB Cooling Fan Motor 5	C7
PCB			
PCB1	Fig.4-7	SIPU	E3
PCB2	Fig.4-8	MCU	D5
PCB3	Fig.4-11	PSU (Power Supply Unit)	B2
PCB4	Fig.6-1	RFDB (Roll Feed Drive Board)	A8
PCB5	Fig.7-1	SFDB (Sheet Feed Drive Board)	C8
PCB6	Fig.2-14	SIF (Scanner Interface Board)	E5
PCB7	Fig.4-5	Mother Board	F2
PCB8	Fig.2-16	VDB (Video Drive Board)	E7
PCB9	Fig.5-1	Controller Board	E2
PCB10	Fig.2-4	Operation Panel	D4
Power Packs			
PP1	Fig.2-11	CGB Power Pack	C6
PP2	Fig.4-3	T&S Power Pack	C5
QL			
QL1 - 3	Fig.1-7	Quenching Lamp (1:Left, 2:Center, 3:Right)	B4

Symbol	Index No.	Description	P to P
Sensors			
S1	Fig.7-5	Cassette End Sensor	C9
S2	Fig.7-4	Cassette Set Sensor	C9
S3	Fig.2-3	Exit Cover Sensor	C7
S4	Fig.3-2	Fusing Exit Sensor	C4
S5	Fig.3-10	ID Sensor	A6
S6	Fig.1-4	Original Exit Sensor	F7
S7	Fig.1-3	Original Registration Sensor	E7
S8	Fig.1-14	Original Set Sensor (A4/8.5” SEF)	E7
S9	Fig.1-9	Original Width Sensor (A0/34”)	F7
S10	Fig.1-11	Original Width Sensor (A1/22”)	F7
S11	Fig.1-12	Original Width Sensor (A2/17”)	F7
S12	Fig.1-13	Original Width Sensor (A3/11”)	F7
S13	Fig.1-8	Original Width Sensor (914mm/36”)	F7
S14	Fig.1-10	Original Width Sensor (30”)	F7
S15	Fig.1-18	Original Width Sensor (B1/24”)	E7
S16	Fig.1-19	Original Width Sensor (B2/18”)	E7
S17	Fig.1-17	Original Width Sensor (B3/12”)	E7
S18	Fig.1-15	Original Width Sensor (B4/9”)	E7
S19	Fig.2-12	Paper Registration Sensor	C7
S20	Fig.2-13	Paper Set Sensor	C7
S21	Fig.7-6	Relay Sensor	D9
S22	Fig.6-12	RF Exit Sensor	B10
S23	Fig.6-14	RF Set Sensor	C9
S24	Fig.6-3	Roll End Sensor 1	A9
S25	Fig.6-4	Roll End Sensor 2	A9
S26	Fig.6-11	Roll End Sensor 3	B10
S27	Fig.6-10	Roll End Sensor 4	C10
S28	Fig.2-8	Toner Overflow Sensor	C4
S29	Fig.2-20	Upper Unit Sensor	B7
S30	Fig.1-16	Wire Cleaner Sensor	C5
Solenoids			
SOL1	Fig.1-5	Paper Junction Gate Solenoid	C5
SOL2	Fig.3-9	Pick-Off Pawl Solenoid	A7
Switches			
SW1	Fig.6-6	Dehumidifier Switch	A3
SW2	Fig.2-7	Exit Cover Switch	C5
SW3	Fig.2-19	Main Power Switch	A2
SW4	Fig.2-6	Scanner Stop Switch	C6
SW5	Fig.1-20	Scanner Switches	G7
SW6	Fig.2-15	Toner Hopper Cover Switch	C3
SW7	Fig.2-9	Upper Unit Switch 1	C3
SW8	Fig.2-9	Upper Unit Switch 2	C3
SW9	Fig.6-15	Left Cutter HP Switch	B10
SW10	Fig.6-9	Right Cutter HP Switch	B10
SW11	Fig.3-4	Exit Unit Switch	C6
SW12	Fig.1-2	Paper Exit Selection Switch	C5
SW13	Fig.5-3	Anti-condensation Heater Switch	A2

Symbol		Description	P to P
Others			
CO1	Fig.4-6	Recycle Counter	B7
H1	Fig.6-7	Dehumidifier 1 (Front/Right)	B4
H2	Fig.6-8	Dehumidifier 2 (Front/Left)	B4
H3	Fig.6-2	Dehumidifier 3 (Rear/Right)	B4
H4	Fig.6-5	Dehumidifier 4 (Rear/Left)	B4
H5	Fig.4-4	Anti-Condensation Heater (Left)	A3
H6	Fig.4-1	Anti-Condensation Heater (Right)	A3
HDD	Fig.5-2	HDD	E2
TH1	Fig.3-1	Hot Roller Thermistor	A3
TH2	Fig.3-6	Pressure Roller Thermistor 1 (End)	A3
TH3	Fig.3-7	Pressure Roller Thermistor 2 (Center)	A3
TS1	Fig.3-8	Thermostat 1 (199°C)	A2
TS2	Fig.3-5	Thermostat 2 (200°C)	A2
CB	Fig.5-4	Circuit Breaker	B1