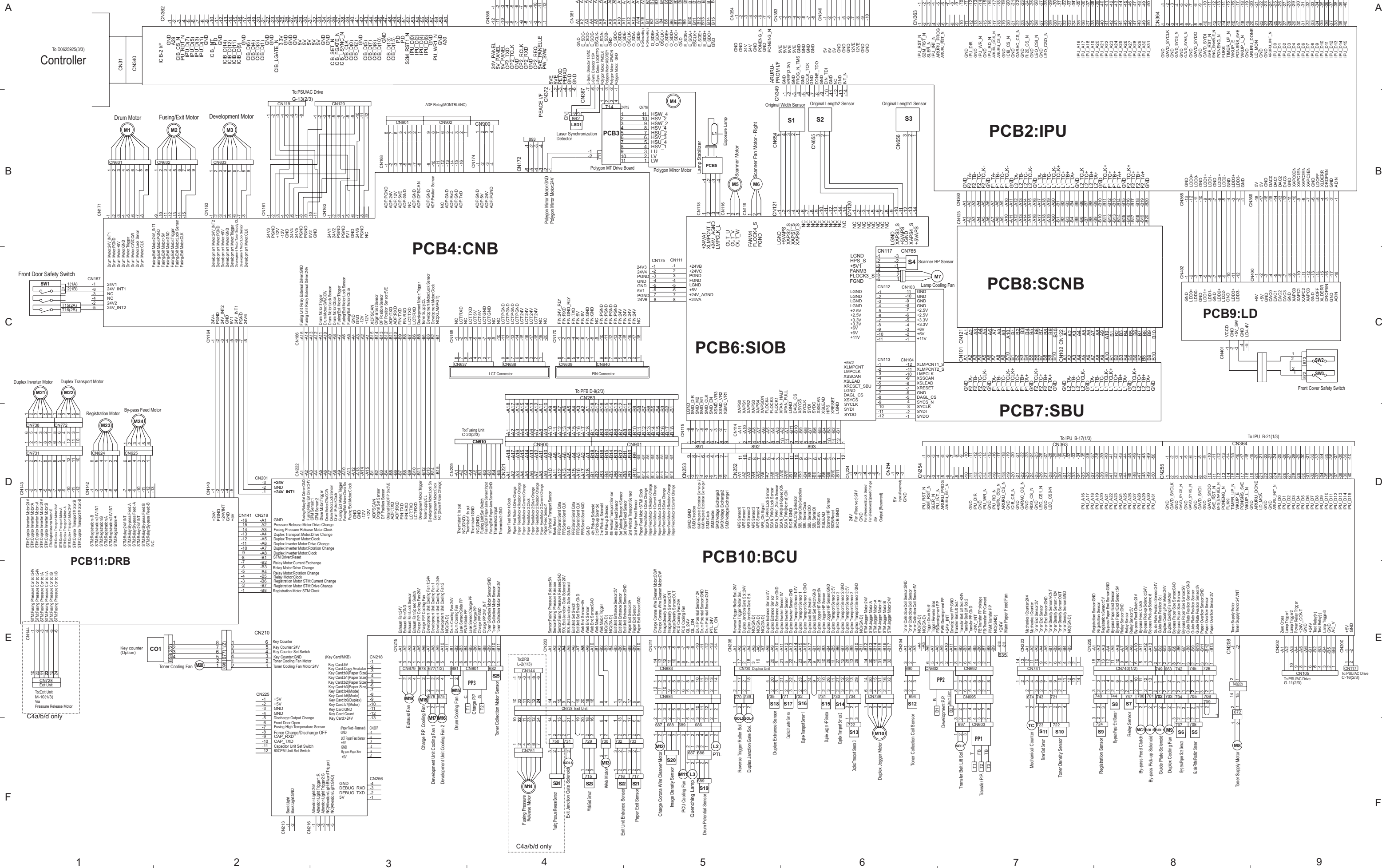
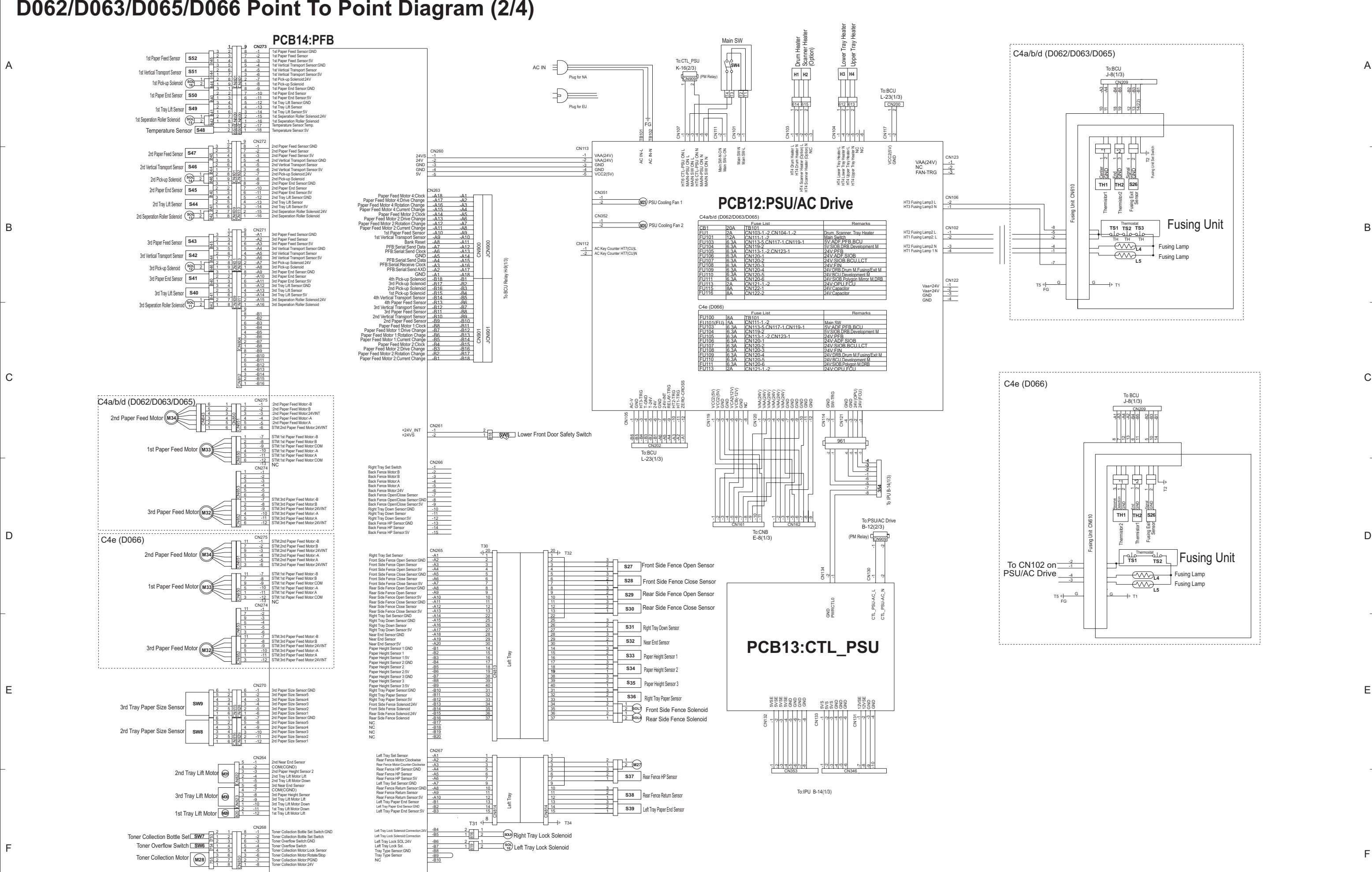


D062/D063/D065/D066 Point To Point Diagram (1/4)





D062/D063/D065/D066 Point To Point Diagram (3/4)

A

B

C

D

E

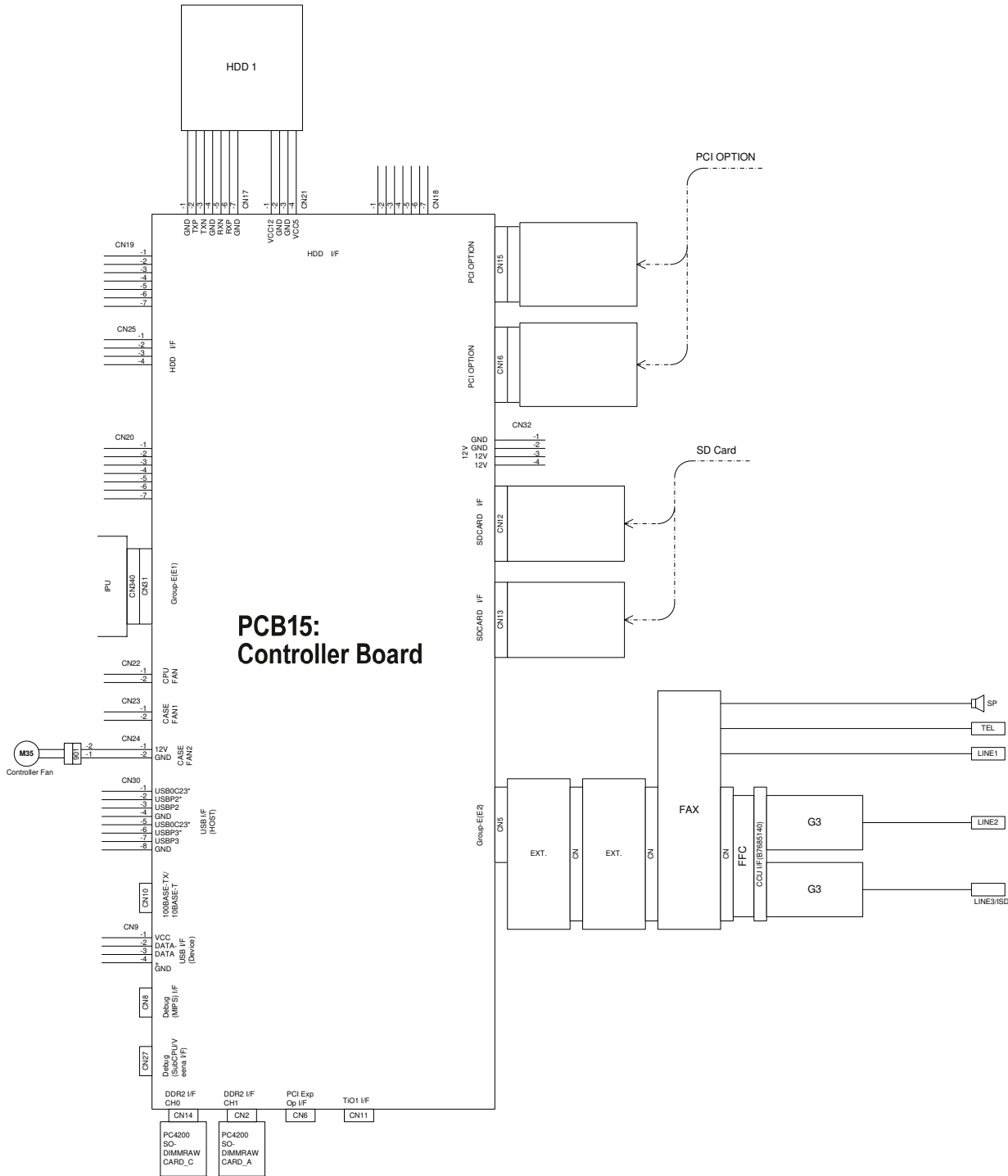
F

G

1 2 3 4 5 6 7 8 9 10

1 2 3 4 5 6 7 8 9 10

1 2 3 4 5 6 7 8 9 10



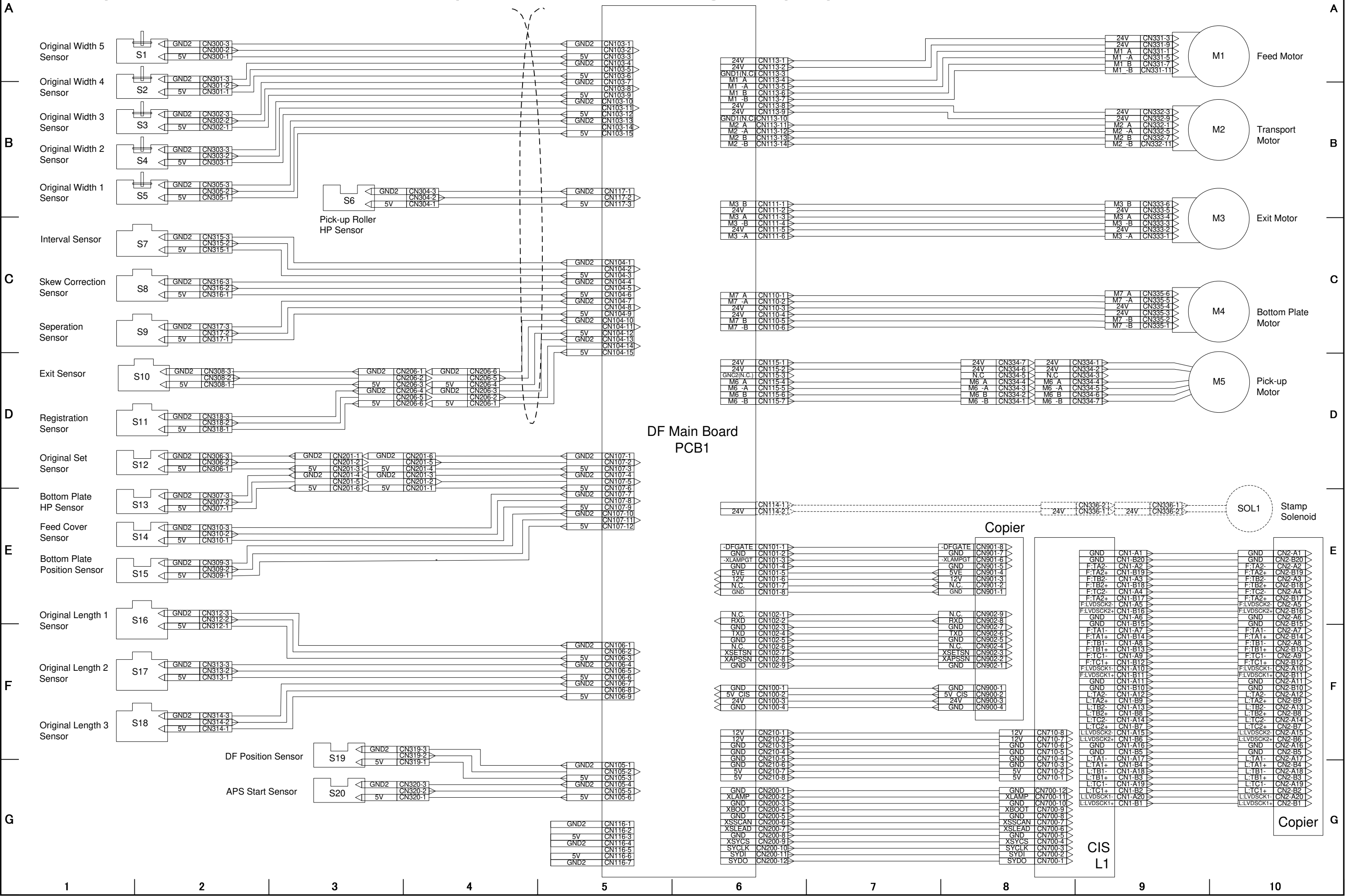
DDR-DIMM															
CN2 DDR-DIMM I/F															
1	VTTREF	26	DM1	51	DQS2	76	DQ(31)	101	MA(1)	126					
2	GND	27	GND	52	DM2	77	GND	102	MA(0)	127					
3	GND	28	GND	53	GND	78	GND	103	VDDQ	128					
4	DQ(4)	29	DQS1*	54	GND	79	CKE0	104	VDDQ	129					
5	DQ(0)	30	CK0	55	DQ(18)	80	CKE1	105	MA(10)	130					
6	DQ(5)	31	DQS1	56	DQ(22)	81	VDDQ	106	BS1	131					
7	DQ(1)	32	CK0*	57	DQ(19)	82	VDDQ	107	BS0	132					
8	GND	33	GND	58	DQ(23)	83	NC	108	RAS*	133					
9	GND	34	GND	59	GND	84	NC	109	WE*	134					
10	DM0	35	DQ(10)	60	GND	85	BS2	110	CS0*	135					
11	DQS0*	36	DQ(14)	61	DQ(24)	86	NC	111	VDDQ	136					
12	GND	37	DQ(11)	62	DQ(28)	87	VDDQ	112	VDDQ	137					
13	DQS0	38	DQ(15)	63	DQ(25)	88	VDDQ	113	CAS*	138					
14	DQ(6)	39	GND	64	DQ(29)	89	MA(12)	114	QD(0)	139					
15	GND	40	GND	65	GND	90	MA(11)	115	CS1*	140					
16	DQ(7)	41	GND	66	GND	91	MA(9)	116	MA(13)	141					
17	DQ(2)	42	GND	67	DM3	92	MA(7)	117	VDDQ	142					
18	GND	43	DQ(16)	68	DQS3*	93	MA(8)	118	VDDQ	143					
19	DQ(3)	44	DQ(20)	69	NC	94	MA(6)	119	QD(1)	144					
20	DQ(12)	45	DQ(17)	70	DQS3	95	VDDQ	120	NC	145					
21	GND	46	DQ(21)	71	GND	96	VDDQ	121	GND	146					
22	DQ(13)	47	GND	72	GND	97	MA(5)	122	GND	147					
23	DQ(8)	48	GND	73	DQ(26)	98	MA(4)	123	DQ(32)	148					
24	GND	49	DQS2*	74	DQ(30)	99	MA(3)	124	DQ(36)	149					
25	DQ(9)	50	NC	75	DQ(27)	100	MA(2)	125	DQ(33)	150					

DDR-DIMM															
CN14 DDR-DIMM I/F															
1	VTTREF	26	DM1	51	DQS2	76	DQ(31)	101	MA(1)	126					
2	GND	27	GND	52	DM2	77	GND	102	MA(0)	127					
3	GND	28	GND	53	GND	78	GND	103	VDDQ	128					
4	DQ(4)	29	DQS1*	54	GND	79	CKE0	104	VDDQ	129					
5	DQ(0)	30	CK0	55	DQ(18)	80	CKE1	105	MA(10)	130					
6	DQ(5)	31	DQS1	56	DQ(22)	81	VDDQ	106	BS1	131					
7	DQ(1)	32	CK0*	57	DQ(19)	82	VDDQ	107	BS0	132					
8	GND	33	GND	58	DQ(23)	83	NC	108	RAS*	133					
9	GND	34	GND	59	GND	84	NC	109	WE*	134					
10	DM0	35	DQ(10)	60	GND	85	BS2	110	CS0*	135					
11	DQS0*	36	DQ(14)	61	DQ(24)	86	NC	111	VDDQ	136					
12	GND	37	DQ(11)	62	DQ(28)	87	VDDQ	112	VDDQ	137					
13	DQS0	38	DQ(15)	63	DQ(25)	88	VDDQ	113	CAS*	138					
14	DQ(6)	39	GND	64	DQ(29)	89	MA(12)	114	QD(0)	139					
15	GND	40	GND	65	GND	90	MA(11)	115	CS1*	140					
16	DQ(7)	41	GND	66	GND	91	MA(9)	116	MA(13)	141					
17	DQ(2)	42	GND	67	DM3	92	MA(7)	117	VDDQ	142					
18	GND	43	DQ(16)	68	DQS3*	93	MA(8)	118	VDDQ	143					
19	DQ(3)	44	DQ(20)	69	NC	94	MA(6)	119	QD(1)	144					
20	DQ(12)	45	DQ(17)	70	DQS3	95	VDDQ	120	NC	145					
21	GND	46	DQ(21)	71	GND	96	VDDQ	121	GND	146					
22	DQ(13)	47	GND	72	GND	97	MA(5)	122	GND	147					
23	DQ(8)	48	GND	73	DQ(26)	98	MA(4)	123	DQ(32)	148					
24	GND	49	DQS2*	74	DQ(30)	99	MA(3)	124	DQ(36)	149					
25	DQ(9)	50	NC	75	DQ(27)	100	MA(2)	125	DQ(33)	150					

Group-E(E2)															
CN5 Group-E(E2)															
1	5VE	26	C/BE2*	51	AD58	76	SD_CMD_ENI*	101	5VE	126	C/BE3*	151	AD59	176	RSVD
2	5VE	27	C/BE0*	52	AD56	77	GND	102	5VE	127	C/BE1*	152	AD57	177	GND
3	5VE	28	P/CRST*	53	GND	78	SDCLK	103	5VE	128	PME*	153	GND	178	SD_DT0
4	5VE	29	DEVSEL*	54	AD54	79	SDCPI*	104	5VE	129	TRDY*	154	AD55	179	SD_DT1
5	5VE	30	IRDY*	55	AD52	80	SD_CMD	105	5VE	130	FRAME*	155	AD53	180	SD_DT2
6	5VE	31	PERR*	56	AD50	81	SDWP	106	5VE	131	STOP*	156	AD51	181	SD_DT3
7	5VE	32	PAR	57	AD48	82	GND	107	5VE	132	SERR*	157	AD49	182	GND
8	5V	33	GND	58	GND	83	OP4_SCLOP0_CLK	108	5VE	133	GND	158	GND	183	ENGRDY1*
9	INTDI	34	AD14	59	AD46	84	OP4_CS/OP0_TXD	109	INTDI	134	AD15	159	AD47	184	PONCTL*
10	INTAI	35	AD12	60	AD44	85	OP4_SDA/OP0_RXD	110	INTAI	135	AD13	160	AD45	185	PW_SWI
11	REQDI	36	AD10	61	AD42	86	OP4_IRQ*	111	REQDI	136	AD11	161	AD43	186	WKUP_L*
12	GNTDI	37	AD8	62	AD40	87	OP4_ONLINE_LED_ON*	112	GNTDI	137	AD9	162	AD41	187	WKUP_E
13	REQI1	38	GND	63	GND	88	TIMER_UP*	113	REQI1	138	GND	163	GND	188	OP4_RST*
14	GNTI1	39	AD6	64	AD38	89	ENG_ENABLE*	114	GNTI1	139	AD7	164	AD39	189	24V
15	GND	40	AD4	65	AD36	90	PONENGI*	115	GND	140	AD5	165	AD37	190	VDET_EPCL
16	AD20	41	AD2	66	AD34	91	PONPCPI*	116	AD20	141	AD3	166	AD35	191	SD_POWER_3.3VE
17	AD28	42	AD0	67	AD32	92	PSAVE_FCU	117	AD28	142	AD1	167	AD33	192	PCICLK_E3
18	AD26	43	GND	68	GND	93	GND	118	AD26	143	GND	168	GND	193	GND
19	AD24	44	C/BE6*	69	PETXD	94	PCICLK_E1	119	AD24	144	C/BE7*	169	PERXD	194	PCICLK_E2
20	GND	45	C/BE4*	70	SDLED/PONSENS*	95	GND	120	GND	145	C/BE5*	170	RSVD	195	GND
21	AD22	46	64REQI	71	OP2_TCLK/OP0_CLK	96	PCICLK_E4	121	AD22	146	64ACKI	171	OP2_RCLK	196	RSVD
22	AD20	47	64PAR	72	OP2_TXD/OP0_TXD	97	GND	122	AD20	147	INTDI	172	OP2_RXD/OP0_RXD	197	GND
23	AD18	48	GND	73	OP2_REQ	98	12V	123	AD18	148	GND	173	SD_CMDDIR	198	12V
24	AD16	49	AD62	74	RSVD	99	12V	124	AD16	149	AD63	174	SD_DTDIR	199	12V
25	GND	50	AD60	75	SD_DT_ENI*	100	12V	125	GND	150	AD61	175	RSVD	200	12V

PCI OPTION																PCI OPTION															
CN15 PCI OPTION UPPER																CN16 PCI OPTION LOWER															
1	GND	26	CBE3*	51	GND	76	reserved	101	GND	126	reserved	151	GND	176	reserved	1	GND	26	CBE3*	51	GND	76	reserved								
2	INTC*	27	GND	52	CBE1*	77	3.3VEP	102	INTC*	127	GND	152	CBE1*	177	3.3VEP	2	INTD*	27	GND	52	CBE1*	77	3.3VEP								
3	GND	28	IOSEL2_PCI	53	AD(15)	78	reserved	103	GND	28	IOSEL3_PCI	53	AD(15)	78	reserved	3	GND	28	IOSEL3_PCI	53	AD(15)	78	reserved								
4	reserved	29	AD(23)	54	AD(14)	79	3.3VEP	104	reserved	29	AD(23)	54	AD(14)	79	3.3VEP	4	reserved	29	AD(23)	54	AD(14)	79	3.3VEP								
5	GND	30	AD(22)	55	GND	80	reserved	105	GND	30	AD(22)	55	GND	80	reserved	5	GND	30	AD(22)	55	GND	80	reserved								
6	CLKRUN	31	GND	56	AD(13)	81	3.3VEP	106	CLKRUN	31	GND	56	AD(13)	81	3.3VEP	6	CLKRUN	31	GND	56	AD(13)	81	3.3VEP								
7	GND	32	AD(21)	57	AD(12)	82	reserved	107	GND	32	AD(21)	57	AD(12)	82	reserved	7	GND	32	AD(21)	57	AD(12)	82	reserved								
8	P/CRST OPT*	33	AD(20)	58	AD(11)	83	5VEP	108	P/CRST OPT*	33	AD(20)	58	AD(11)	83	5VEP	8	P/CRST OPT*	33	AD(20)	58	AD(11)	83	5VEP								
9	GND	34	AD(19)	59	GND	84	reserved	109	GND	34	AD(19)	59	GND	84	reserved	9	GND	34	AD(19)	59	GND	84	reserved								
10	PCLK OPT1	35	GND	60	AD(10)	85	5VEP	110	PCLK OPT1	35	GND	60	AD(10)	85	5VEP	10	PCLK OPT1	35	GND	60	AD(10)	85	5VEP								
11	GND	36	AD(18)	61	AD(9)	86	reserved	111	GND	36	AD(18)	61	AD(9)	86	reserved	11	GND	36	AD(18)	61	AD(9)	86	reserved								
12	GNTI*	37	AD(17)	62	AD(8)	87	3.3VE WSL	112	GNTI*	37	AD(17)	62	AD(8)	87	3.3VE WSL	12	GNTI*	37	AD(17)	62	AD(8)	87	3.3VE WSL								
13	GND	38	AD(16)	63	GND	88	+24V	113	GND	38	AD(16)	63	GND	88	+24V	13	GND	38	AD(16)	63	GND	88	+24V								
14	REQI*	39	GND	64	CBE0*	89	AD(7)	114	REQI*	39	GND	64	CBE0*	89	AD(7)	14	REQI*	39	GND	64	CBE0*	89	AD(7)								
15	GND	40	CBE2*	65	AD(7)	90	AD(6)	115	GND	40	CBE2*	65	AD(7)	90	AD(6)	15	GND	40	CBE2*	65	AD(7)	90	AD(6)								
16	PME*	41	FRAME*	66	AD(6)	91	AD(5)	116	PME*	41	FRAME*	66	AD(6)	91	AD(5)	16	PME*	41	FRAME*	66	AD(6)	91	AD(5)								
17	AD(31)	42	IRDY*	67	AD(5)	92	AD(4)	117	AD(31)	42	IRDY*	67	AD(5)	92	AD(4)	17	AD(31)	42	IRDY*	67	AD(5)	92	AD(4)								
18	AD(30)	43	GND	68	AD(4)	93	AD(3)	118	AD(30)	43	GND	68	AD(4)	93	AD(3)	18	AD(30)	43	GND	68	AD(4)	93	AD(3)								
19	AD(29)	44	TRDY*	69	AD(3)	94	AD(2)	119	AD(29)	44	TRDY*	69	AD(3)	94	AD(2)	19	AD(29)	44	TRDY*	69	AD(3)	94	AD(2)								
20	AD(28)	45	DEVSSEL*	70	AD(2)	95	DEVSSEL*	120	AD(28)	45	DEVSSEL*	70	AD(2)	95	DEVSSEL*	20	AD(28)	45	DEVSSEL*	70	AD(2)	95	DEVSSEL*								
21	AD(27)	46	STOP*	71	3.3VEP	96	STOP*	121	AD(27)	46	STOP*	71	3.3VEP	96	STOP*	21	AD(27)	46	STOP*	71	3.3VEP	96	STOP*								
22	AD(26)	47	GND	72	AD(1)	97	GND	122	AD(26)	47	GND	72	AD(1)	97	GND	22	AD(26)	47	GND	72	AD(1)	97	GND								
23	GND	48	PERB*	73	3.3VEP	98	PERB*	123	GND	48	PERB*	73	3.3VEP	98	PERB*	23	GND	48	PERB*	73	3.3VEP	98	PERB*								
24	AD(25)	49	SERR*	74	AD(0)	99	SERR*	124	AD(25)	49	SERR*	74	AD(0)	99	SERR*	24	AD(25)	49	SERR*	74	AD(0)	99	SERR*								
25	AD(24)	50	PAR	75	3.3VEP	100	PAR	125	AD(24)	50	PAR	75	3.3VEP	100	PAR	25	AD(24)	50	PAR	75	3.3VEP	100	PAR								

ADF (FOR D062/D063/D065/D066) Point To Point Diagram (4/4)



D062/D063/D065/D066 ELECTRICAL COMPONENT LAYOUT (1/4)

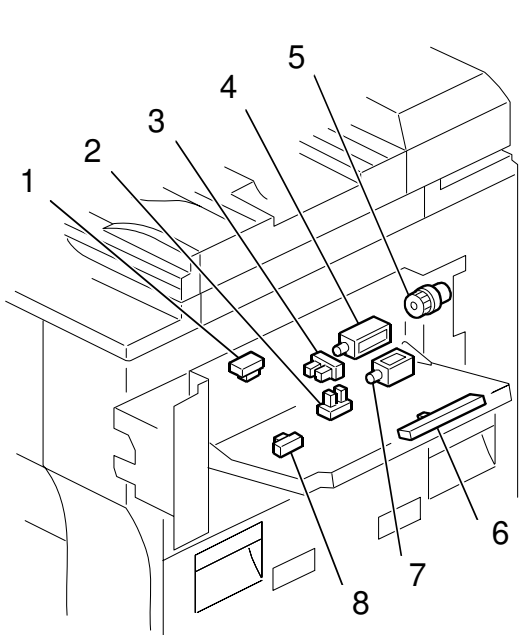


Fig1 D062V003.WMF

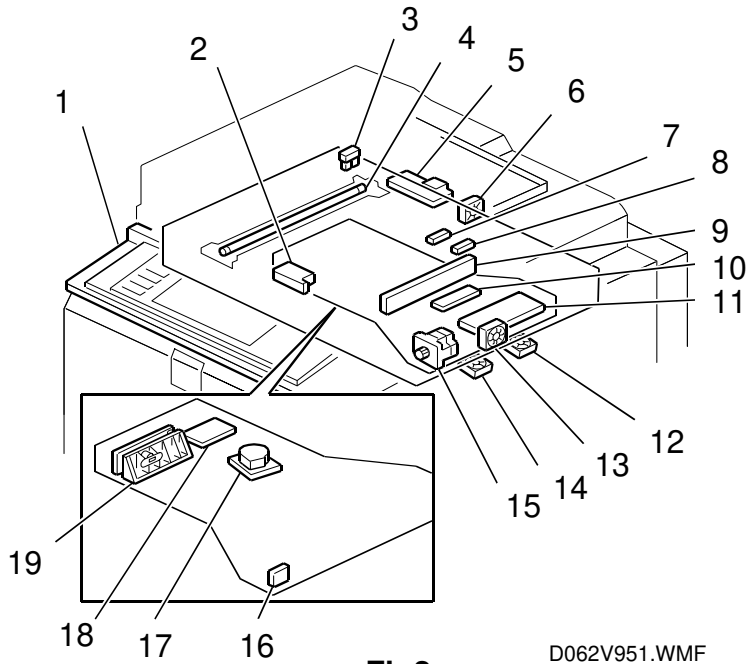
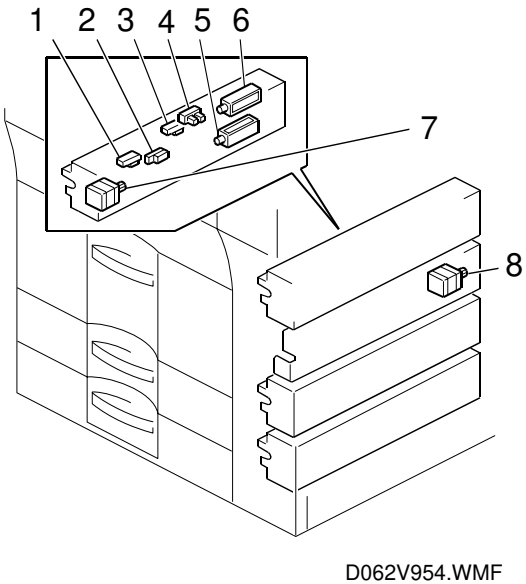
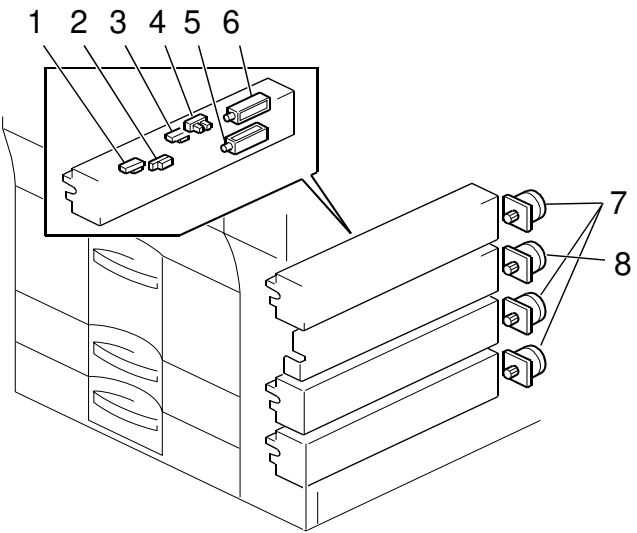


Fig2 D062V951.WMF



D062V954.WMF



D062V954A.WMF

Fig3

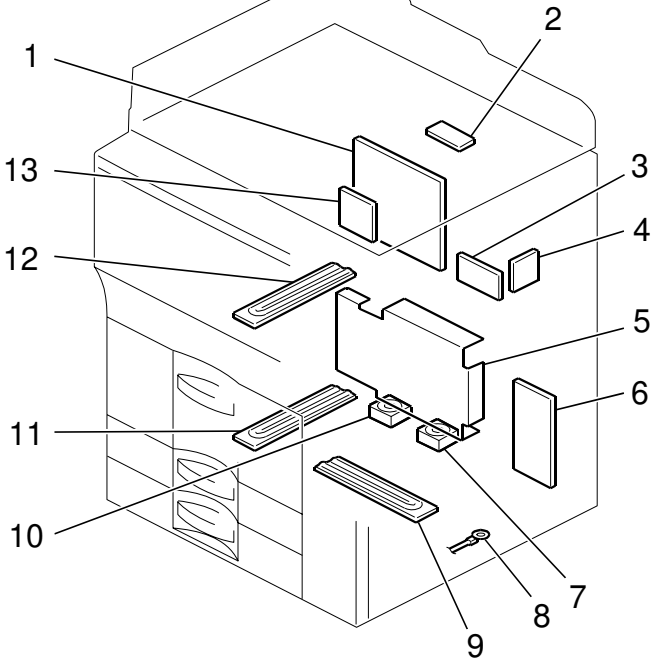


Fig4 D062V955A.WMF

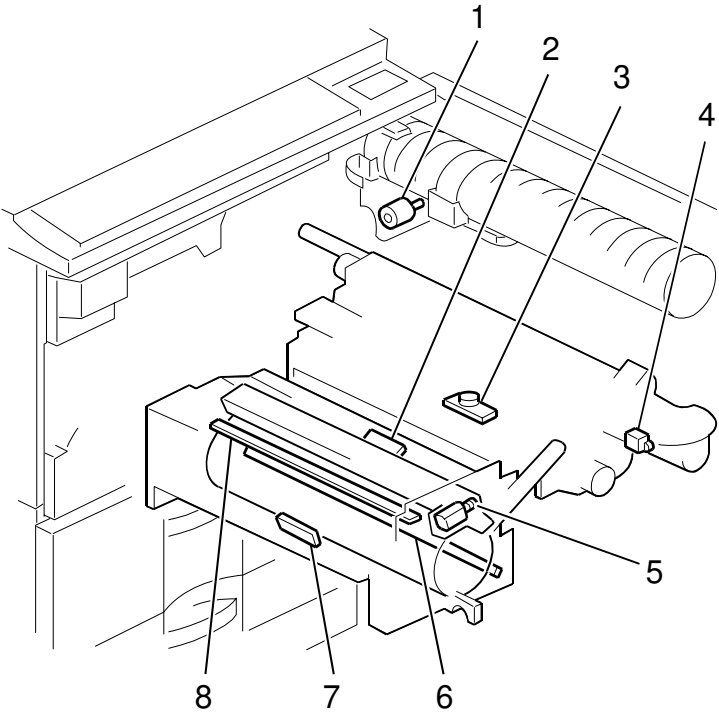


Fig5 D062V008.WMF

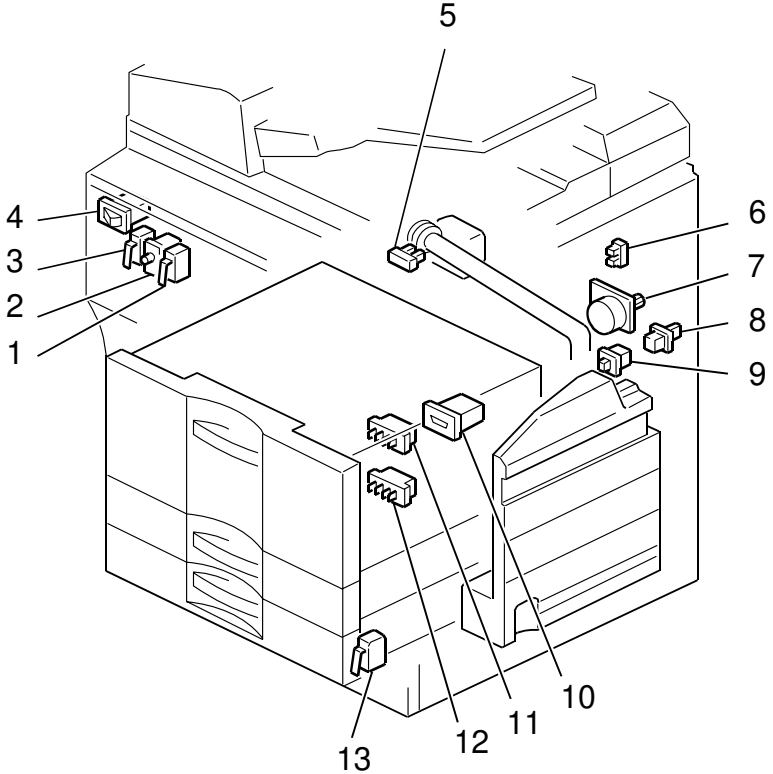
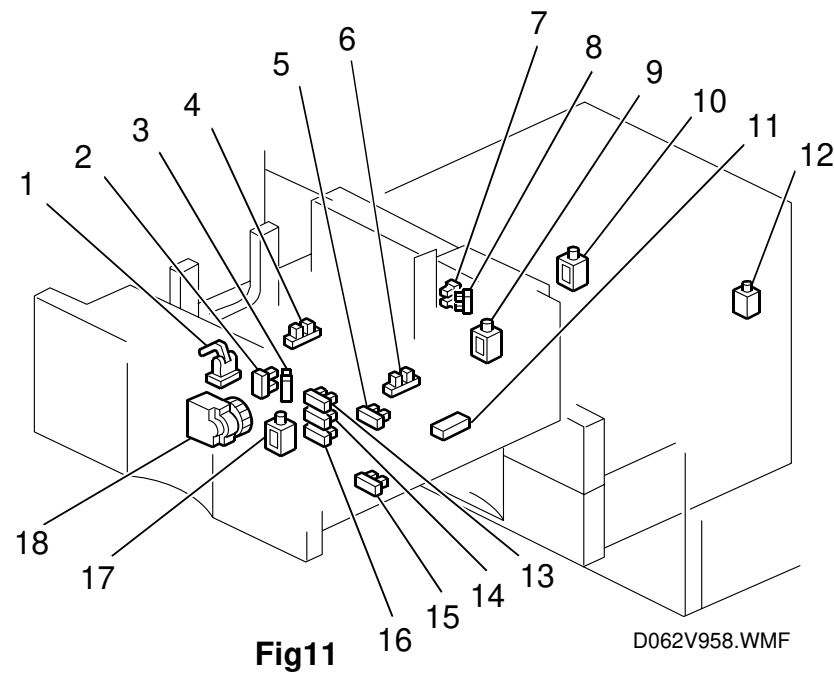
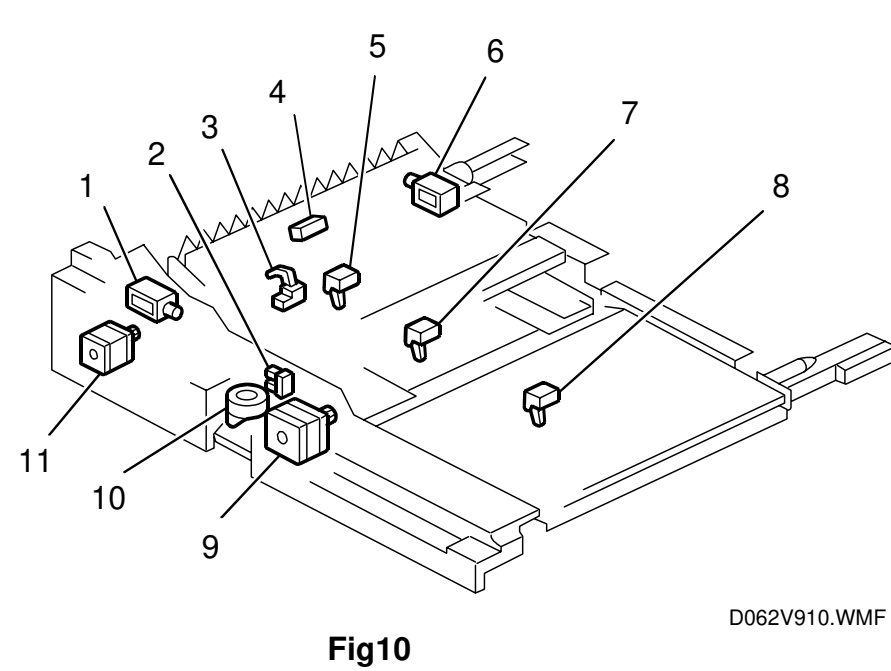
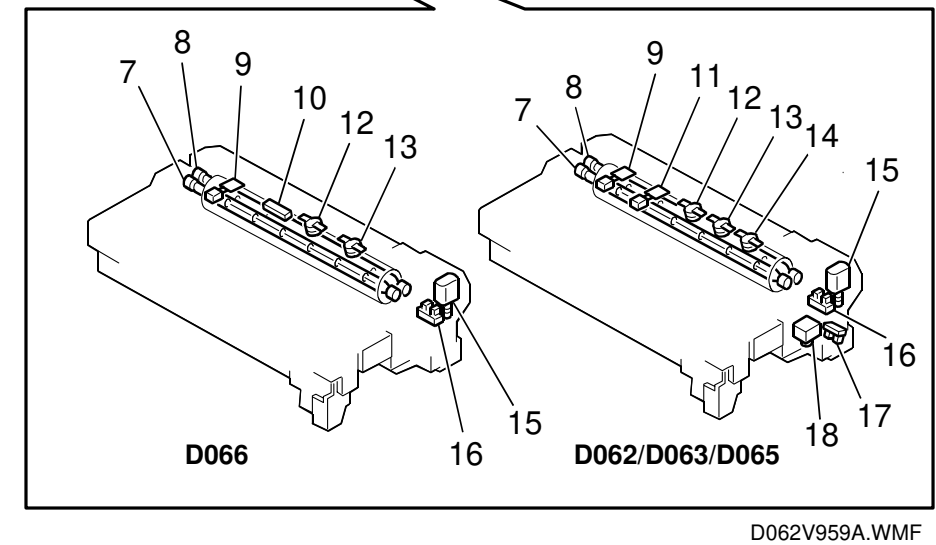
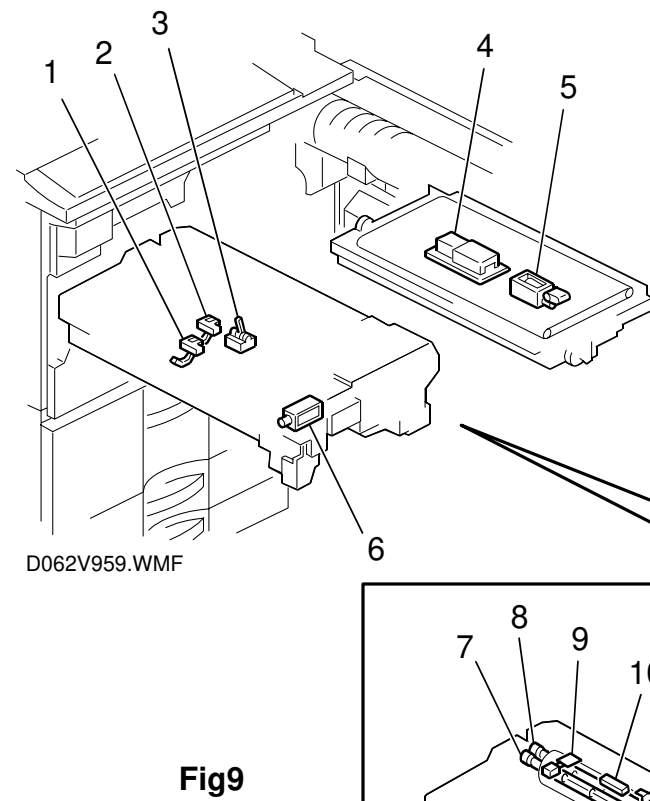
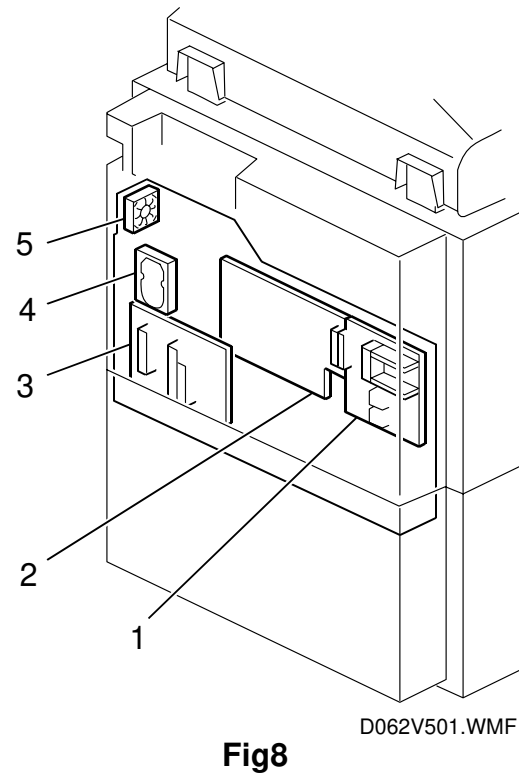
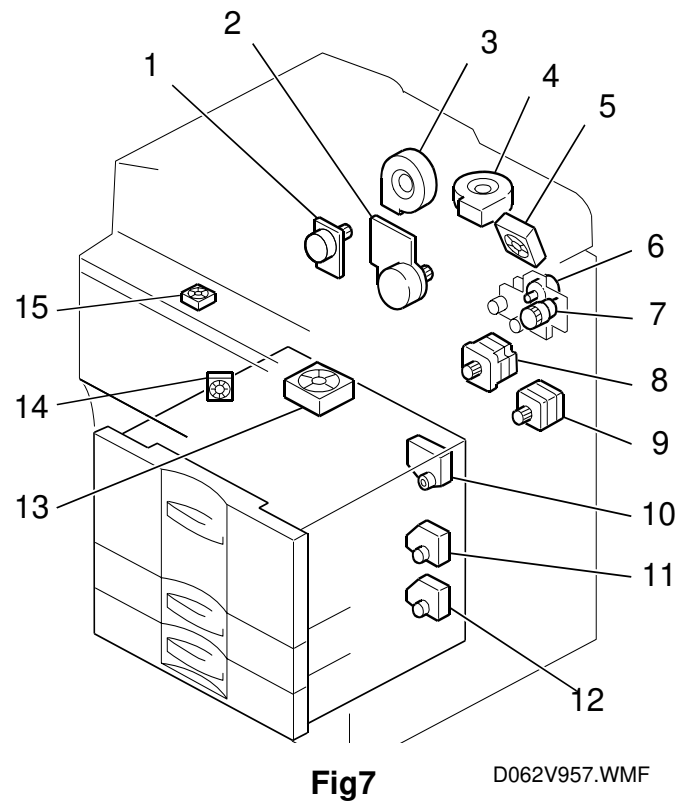


Fig6 D062V956.WMF

D062/D063/D065/D066 ELECTRICAL COMPONENT LAYOUT (2/4)



D062/D063/D065/D066 ELECTRICAL COMPONENT LAYOUT (3/4)

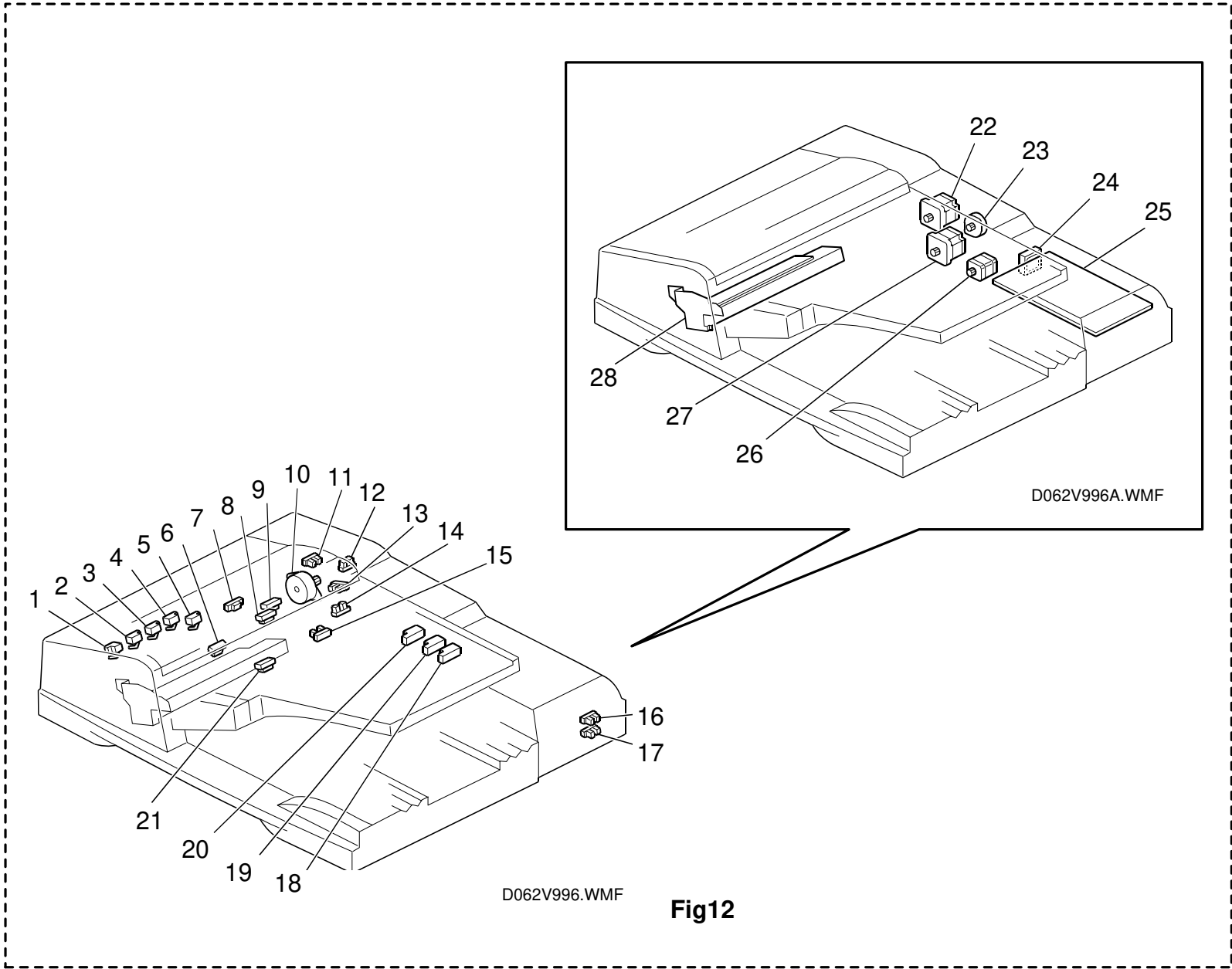
Note; C4a: D062, C4b: D063, C4d: D065, C4e: D066

Symbol	Index No.		Description	P to P	
	a/b/d model	e model		a/b/d model	e model
Motors					
M1	Fig7—2	Fig7—2	Drum	B1 (1/4)	B1 (1/4)
M2	Fig7—1	Fig7—1	Fusing/Exit	B2 (1/4)	B2 (1/4)
M3	Fig7—6	Fig7—6	Development	B2 (1/4)	B2 (1/4)
M4	Fig2—17	Fig2—17	Polygonal Mirror	B5 (1/4)	B5 (1/4)
M5	Fig2—15	Fig2—15	Scanner	B5 (1/4)	B5 (1/4)
M6	Fig2—13	Fig2—13	Scanner Fan Motor - Right	B5 (1/4)	B5 (1/4)
M7	Fig2—6	Fig2—6	Lamp Cooling Fan	C7 (1/4)	C7 (1/4)
M8	Fig5—1	Fig5—1	Toner Supply	F8 (1/4)	F8 (1/4)
M9	Fig7—13	Fig7—13	Duplex Cooling Fan	F8 (1/4)	F8 (1/4)
M10	Fig10—10	Fig10—10	Duplex Jogger	F6 (1/4)	F6 (1/4)
M11	Fig7—14	Fig7—14	PCU Cooling Fan	F5 (1/4)	F5 (1/4)
M12	Fig5—5	Fig5—5	Charge Corona Wire Cleaner	F5 (1/4)	F5 (1/4)
M13	Fig9—15	Fig9—15	Web	F4 (1/4)	F4 (1/4)
M14	Fig9—18	Not used	Fusing Pressure Release Motor	F4 (1/4)	—
M15	Fig7—5	Fig7—5	Drum Cooling Fan	F3 (1/4)	F3 (1/4)
M16	Fig2—14	Fig2—14	Development Unit Cooling Fan 2	F3 (1/4)	F3 (1/4)
M17	Fig2—12	Fig2—12	Development Unit Cooling Fan 1	F3 (1/4)	F3 (1/4)
M18	Fig7—4	Fig7—4	Charge Power Pack Cooling Fan	F3 (1/4)	F3 (1/4)
M19	Fig7—3	Fig7—3	Exhaust Fan	F3 (1/4)	F3 (1/4)
M20	Fig7—15	Fig7—15	Toner Cooling Fan	E2 (1/4)	E2 (1/4)
M21	Fig10—11	Fig10—11	Duplex Inverter	C1 (1/4)	C1 (1/4)
M22	Fig10—9	Fig10—9	Duplex Transport	C1 (1/4)	C1 (1/4)
M23	Fig7—8	Fig7—8	Registration	D1 (1/4)	D1 (1/4)
M24	Fig7—9	Fig7—9	By-pass Feed	D1 (1/4)	D1 (1/4)
M25	Fig4—7	Fig4—7	PSU Cooling Fan 1	B5 (2/4)	B5 (2/4)
M26	Fig4—10	Fig4—10	PSU Cooling Fan 2	B5 (2/4)	B5 (2/4)
M27	Fig11—18	Fig11—18	Rear Fence	F5 (2/4)	F5 (2/4)
M28	Fig6—7	Fig6—7	Toner Collection	F2 (2/4)	F2 (2/4)
M29	Fig7—10	Fig7—10	1st Tray Lift	F2 (2/4)	F2 (2/4)
M30	Fig7—12	Fig7—12	3rd Tray Lift	F2 (2/4)	F2 (2/4)
M31	Fig7—11	Fig7—11	2nd Tray Lift	E2 (2/4)	E2 (2/4)
M32	Fig3—7	Fig3—7	3rd Paper Feed	F2 (2/4)	F2 (2/4)
M33	Fig3—7	Fig3—7	1st Paper Feed	D2 (2/4)	D2 (2/4)
M34	Fig3—7	Fig3—7	2nd Paper Feed	D2 (2/4)	D2 (2/4)
M35	Fig8—5	Fig8—5	Controller Fan	D1 (3/4)	D1 (3/4)
—	Fig3—8	Fig3—8	Lower Relay	—	—
Sensors					
S1	Fig2—2	Fig2—2	Original Width	B6 (1/4)	B6 (1/4)
S2	Fig2—8	Fig2—8	Original Length 2	B6 (1/4)	B6 (1/4)
S3	Fig2—7	Fig2—7	Original Length 1	B6 (1/4)	B6 (1/4)
S4	Fig2—3	Fig2—3	Scanner HP	C6 (1/4)	C6 (1/4)
S5	Fig1—3	Fig1—3	Guide Plate Position	F8 (1/4)	F8 (1/4)
S6	Fig1—6	Fig1—6	By-pass Paper Size	F8 (1/4)	F8 (1/4)
S7	Fig1—8	Fig1—8	Relay	F8 (1/4)	F8 (1/4)
S8	Fig1—2	Fig1—2	By-pass Paper End	F8 (1/4)	F8 (1/4)
S9	Fig1—1	Fig1—1	Registration	F8 (1/4)	F8 (1/4)
S10	Fig5—3	Fig5—3	Toner Density (TD)	F7 (1/4)	F7 (1/4)
S11	Fig5—4	Fig5—4	Toner End	F7 (1/4)	F7 (1/4)
S12	Fig6—5	Fig6—5	Toner Collection Coil	F6 (1/4)	F6 (1/4)
S13	Fig10—8	Fig10—8	Duplex Transport 3	F6 (1/4)	F6 (1/4)
S14	Fig10—7	Fig10—7	Duplex Transport 2	F6 (1/4)	F6 (1/4)
S15	Fig10—2	Fig10—2	Duplex Jogger HP	F6 (1/4)	F6 (1/4)
S16	Fig10—5	Fig10—5	Duplex Transport 1	F6 (1/4)	F6 (1/4)
S17	Fig10—4	Fig10—4	Duplex Inverter	F6 (1/4)	F6 (1/4)
S18	Fig10—5	Fig10—5	Duplex Entrance	F5 (1/4)	F5 (1/4)
S19	Fig5—2	Fig5—2	Drum Potential	F5 (1/4)	F5 (1/4)
S20	Fig5—7	Fig5—7	Image Density (ID)	F5 (1/4)	F5 (1/4)
S21	Fig9—1	Fig9—1	Paper Exit	F5 (1/4)	F5 (1/4)

Symbol	Index No.		Description	P to P	
	a/b/d model	e model		a/b/d model	e model
S22	Fig9-2	Fig9-2	Exit Unit Entrance	F4 (1/4)	F4 (1/4)
S23	Fig9-16	Fig9-16	Web End	F4 (1/4)	F4 (1/4)
S24	Fig19-17	Not used	Fusing Pressure Release Sensor	F4 (1/4)	—
S25	Fig6-6	Fig6-6	Toner Collection Motor	E4 (1/4)	E4 (1/4)
S26	Fig9-3	Fig9-3	Fusing Exit	B8 (2/4)	D8 (2/4)
S27	Fig11-3	Fig11-3	Front Side Fence Open	D5 (2/4)	D5 (2/4)
S28	Fig11-2	Fig11-2	Front Side Fence Close	D5 (2/4)	D5 (2/4)
S29	Fig11-8	Fig11-8	Rear Side Fence Open	D5 (2/4)	D5 (2/4)
S30	Fig11-7	Fig11-7	Rear Side Fence Close	D5 (2/4)	D5 (2/4)
S31	Fig11-15	Fig11-15	Right Tray Down	E5 (2/4)	E5 (2/4)
S32	Fig11-5	Fig11-5	Near End	E5 (2/4)	E5 (2/4)
S33	Fig11-13	Fig11-13	Paper Height 1	E5 (2/4)	E5 (2/4)
S34	Fig11-14	Fig11-14	Paper Height 2	E5 (2/4)	E5 (2/4)
S35	Fig11-16	Fig11-16	Paper Height 3	E5 (2/4)	E5 (2/4)
S36	Fig11-11	Fig11-11	Right Tray Paper	E5 (2/4)	E5 (2/4)
S37	Fig11-4	Fig11-4	Rear Fence HP	F5 (2/4)	F5 (2/4)
S38	Fig11-6	Fig11-6	Rear Fence Return	F5 (2/4)	F5 (2/4)
S39	Fig11-1	Fig11-1	Left Tray Paper End	F5 (2/4)	F5 (2/4)
S40	Fig3-4	Fig3-4	3rd Tray Lift	B2 (2/4)	B2 (2/4)
S41	Fig3-3	Fig3-3	3rd Paper End	B2 (2/4)	B2 (2/4)
S42	Fig3-2	Fig3-2	3rd Vertical Transport	B2 (2/4)	B2 (2/4)
S43	Fig3-1	Fig3-1	3rd Paper Feed	B2 (2/4)	B2 (2/4)
S44	Fig3-4	Fig3-4	2nd Tray Lift	B2 (2/4)	B2 (2/4)
S45	Fig3-3	Fig3-3	2nd Paper End	B2 (2/4)	B2 (2/4)
S46	Fig3-2	Fig3-2	2nd Vertical Transport	B2 (2/4)	B2 (2/4)
S47	Fig3-1	Fig3-1	2nd Paper Feed	B2 (2/4)	B2 (2/4)
S48	Fig4-8	Fig4-8	Temperature Sensor	A2 (2/4)	A2 (2/4)
S49	Fig3-4	Fig3-4	1st Tray Lift	A2 (2/4)	A2 (2/4)
S50	Fig3-3	Fig3-3	1st Paper End	A2 (2/4)	A2 (2/4)
S51	Fig3-2	Fig3-2	1st Vertical Transport	A2 (2/4)	A2 (2/4)
S52	Fig3-1	Fig3-1	1st Paper Feed	A2 (2/4)	A2 (2/4)
Switches					
SW1	Fig6-2	Fig6-2	Front Door Safety	C1 (1/4)	C1 (1/4)
SW2	Fig6-1	Fig6-1	Front Cover Safety 1	C9 (1/4)	C9 (1/4)
SW3	Fig6-3	Fig6-3	Front Cover Safety 2	C9 (1/4)	C9 (1/4)
SW4	Fig6-4	Fig6-4	Main	A5 (2/4)	A5 (2/4)
SW5	Fig6-13	Fig6-13	Lower Front Door Safety	D4 (2/4)	D4 (2/4)
SW6	Fig6-8	Fig6-8	Toner Overflow	F2 (2/4)	F2 (2/4)
SW7	Fig6-9	Fig6-9	Toner Collection Bottle Set	F2 (2/4)	F2 (2/4)
SW8	Fig6-11	Fig6-11	2nd Paper Size	E2 (2/4)	E2 (2/4)
SW9	Fig6-12	Fig6-12	3rd Paper Size	E2 (2/4)	E2 (2/4)
Solenoids					
SOL1	Fig1-7	Fig1-7	Guide Plate	F8 (1/4)	F8 (1/4)
SOL2	Fig1-4	Fig1-4	By-pass Pick-up	F8 (1/4)	F8 (1/4)
SOL3	Fig9-5	Fig9-5	Transfer Belt Lift	F7 (1/4)	F7 (1/4)
SOL4	Fig10-6	Fig10-6	Duplex Junction Gate	F5 (1/4)	F5 (1/4)
SOL5	Fig10-1	Fig10-1	Reverse Trigger Roller	F5 (1/4)	F5 (1/4)
SOL6	Fig9-6	Fig9-6	Exit Junction Gate	F4 (1/4)	F4 (1/4)
SOL7	Fig11-17	Fig11-17	Front Side Fence	E5 (2/4)	E5 (2/4)
SOL8	Fig11-9	Fig11-9	Rear Side Fence	E5 (2/4)	E5 (2/4)
SOL9	Fig11-12	Fig11-12	Right Tray Lock	F4 (2/4)	F4 (2/4)
SOL10	Fig11-10	Fig11-10	Left Tray Lock	F4 (2/4)	F4 (2/4)
SOL11	Fig3-5	Fig3-5	3rd Separation Roller	B2 (2/4)	B2 (2/4)
SOL12	Fig3-6	Fig3-6	3rd Pick-up	B2 (2/4)	B2 (2/4)
SOL13	Fig3-5	Fig3-5	2nd Separation Roller	B2 (2/4)	B2 (2/4)
SOL14	Fig3-6	Fig3-6	2nd Pick-up	B2 (2/4)	B2 (2/4)
SOL15	Fig3-5	Fig3-5	1st Separation Roller	A2 (2/4)	A2 (2/4)
SOL16	Fig3-6	Fig3-6	1st Pick-up	A2 (2/4)	A2 (2/4)

Symbol	Index No.		Description	P to P	
	a/b/d model	e model		a/b/d model	e model
Magnetic Clutches					
MC1	Fig1—5	Fig1—5	By-pass Feed	F8 (1/4)	F8 (1/4)
—	Fig7—7	Fig7—7	Toner Supply	B2 (1/4)	B2 (1/4)
PCBs					
PCB1	Fig2—1	Fig2—1	OPU	A4 (1/4)	A4 (1/4)
PCB2	Fig8—2	Fig8—2	IPU (Image Processing Control Unit)	B7 (1/4)	B7 (1/4)
PCB3	Fig2—18	Fig2—18	Polygon Motor Drive Board	B4 (1/4)	B4 (1/4)
PCB4	Fig4—13	Fig4—13	CNB (Connection Board)	C3 (1/4)	C3 (1/4)
PCB5	Fig2—5	Fig2—5	Lamp Stabilizer	B5 (1/4)	B5 (1/4)
PCB6	Fig2—11	Fig2—11	SIOB	C5 (1/4)	C5 (1/4)
PCB7	Fig2—9	Fig2—9	SBU (Sensor Board Unit)	C7 (1/4)	C7 (1/4)
PCB8	Fig2—10	Fig2—10	SCNB	C7 (1/4)	C7 (1/4)
PCB9	Fig2—19	Fig2—19	LDB (Laser Drive Board)	C8 (1/4)	C8 (1/4)
PCB10	Fig4—1	Fig4—1	BCU (Base Engine Control Unit)	D5 (1/4)	D5 (1/4)
PCB11	Fig4—3	Fig4—3	DRB (Drive Board)	D1 (1/4)	D1 (1/4)
PCB12	Fig4—5	Fig4—5	PSU	B6 (2/4)	B6 (2/4)
PCB13	Fig8—3	Fig8—3	Controller PSU	E6 (2/4)	E6 (2/4)
PCB14	Fig4—6	Fig4—6	PFB (Paper Feed Board)	A2 (2/4)	A2 (2/4)
PCB15	Fig8—1	Fig8—1	Controller	D2 (3/4)	D2 (3/4)
Lamps					
L1	Fig2—4	Fig2—4	Exposure Lamp	B5 (1/4)	B5 (1/4)
L2	Fig5—6	Fig5—6	PTL	F5 (1/4)	F5 (1/4)
L3	Fig5—8	Fig5—8	Quenching Lamp	F5 (1/4)	F5 (1/4)
L4	Fig9—7	Fig9—7	Fusing Lamp 1	B8 (2/4)	D8 (2/4)
L5	Fig9—8	Fig9—8	Fusing Lamp 2	B8 (2/4)	D8 (2/4)
Power Packs					
PP1	Fig9—4	Fig9—4	Transfer	F7 (1/4)	F7 (1/4)
PP2	Fig4—4	Fig4—4	Development	E7 (1/4)	E7 (1/4)
PP3	Fig4—2	Fig4—2	Charge	E4 (1/4)	E4 (1/4)
Others					
CO1	—	—	Key Counter (Option)	E2 (1/4)	E2 (1/4)
TC	Fig6—10	Fig6—10	Mechanical Counter	F7 (1/4)	F7 (1/4)
H1	Fig4—12	Fig4—12	Drum	A6 (2/4)	A6 (2/4)
H2	—	—	Scanner (Option)	A6 (2/4)	A6 (2/4)
H3	Fig4—9	Fig4—9	Lower Tray	A6 (2/4)	A6 (2/4)
H4	Fig4—11	Fig4—11	Upper Tray	A6 (2/4)	A6 (2/4)
LSD1	Fig2—16	Fig2—16	Laser Synchronization Detector	B4 (1/4)	B4 (1/4)
TH1	Not used	Fig9—10	Thermisotr 1 (not contact the hot roller)	—	D8 (2/4)
TH1	Fig9—11	Not used	Thermisotr 1 (contact the hot roller)	B8 (2/4)	—
TH2	Fig9—9	Fig9—9	Thermistor 2	B8 (2/4)	D8 (2/4)
TS1	Fig9—12	Fig9—12	Thermostat 1	B8 (2/4)	D8 (2/4)
TS2	Fig9—13	Fig9—13	Thermostat 2	B8 (2/4)	D8 (2/4)
TS3	Fig9—14	Not used	Thermostat 3	B8 (2/4)	—
HDD1	Fig8—4	Fig8—4	HDD	B2 (3/4)	B2 (3/4)

ADF (FOR D062/D063/D065/D066) ELECTRICAL COMPONENT LAYOUT (4/4)



Symbol	Index No.	Description	P to P
Motors			
M1	Fig12-22	Feed	A10 (4/4)
M2	Fig12-27	Transport	B10 (4/4)
M3	Fig12-26	Exit	B10 (4/4)
M4	Fig12-23	Bottom Plate	C10 (4/4)
M5	Fig12-10	Pick-up	D10 (4/4)
—	Fig12-24	ADF Fan	—
Sensors			
S1	Fig12-1	Original Width 5	A1 (4/4)
S2	Fig12-2	Original Width 4	B1 (4/4)
S3	Fig12-3	Original Width 3	B1 (4/4)
S4	Fig12-4	Original Width 2	B1 (4/4)
S5	Fig12-5	Original Width 1	B1 (4/4)
S6	Fig12-11	Pick-up Roller HP	B3 (4/4)
S7	Fig12-7	Interval	C1 (4/4)
S8	Fig12-9	Skew Correction	C1 (4/4)
S9	Fig12-8	Seperation Sensor	C1 (4/4)
S10	Fig12-21	Exit	D1 (4/4)
S11	Fig12-6	Registration	D1 (4/4)
S12	Fig12-14	Original Set	D1 (4/4)
S13	Fig12-15	Bottom Plate HP	E1 (4/4)
S14	Fig12-12	Feed Cover	E1 (4/4)
S15	Fig12-13	Bottom Plate Position	E1 (4/4)
S16	Fig12-18	Original Length 1	F1 (4/4)
S17	Fig12-19	Original Length 2	F1 (4/4)
S18	Fig12-20	Original Length 3	F1 (4/4)
S19	Fig12-16	DF Position	G3 (4/4)
S20	Fig12-17	APS Start	G3 (4/4)
Solenoids			
SOL1	—	Stamp Solenoid	E10 (4/4)
PCBs			
PCB1	Fig12-25	DF Main	D6 (4/4)
Lamps			
L1	Fig12-28	CIS	G9 (4/4)